

# CRYSTAL CLEAR TECHNOLOGY

## Product Specification

**T700A16N00**

(REVISION 2)

**Crystal Clear Technology sdn. bhd.**

16 Jalan TP5, Taman Perindustrian Sime UEP,  
47600 Subang Jaya, Selangor DE  
Malaysia.

T: +603 80247099

Website: [www.cct.com.my](http://www.cct.com.my)

---





## 2. Record of Revision

Rev	Date	Item	Page	Comment	Prepared	Checked
REV1	07/01/19			Initial Release	Othman	WHong
REV2	14/01/19	3	4	Remove HDMI board & CTP table	Othman	WHong
				Change outline dimensions		
		3	5	Change ordering information		
		4.2	6	Revise electrical characteristic		
		4.4	7	Add power sequence		
		4.5	8	Add LVDS Interface		
		4.6	9	Add LVDS Signal Timing		
		4.7	10	Add data input format		
		4.8	11	Add signal timing specification		
4.9	12	Change from item 4.4 to 4.9	Othman	WHong		



### 3. General Specification

T700A16N00 is 7.0" color TFT-LCD (Thin Film Transistor Liquid Crystal Display) module composed of LCD panel, driver ICs control circuit, LED backlight & Capacitive Touch Panel (CTP). By applying 1024x600 images are displayed on the 7.0" diagonal screen. Display 16.7M colors by R.G.B signal input.

General Specification are summarized in the following table:

Item	Specifications	Unit	Note
Size (Diagonal)	7.0	inch	
Outline dimensions	164.80(W) x 99.80(H) x 5.60(T)	mm	(1)
TFT	Display type	16.7M color TFT, Normally Black	- (2)
	Viewing direction	All Direction	O'clock
	TFT Active area	154.21(W) x 85.92(H)	mm
	TFT Resolution	1024(RGB) x 600	-
	Pixel size	0.1506(W) x 0.1432(H)	mm
	Pixel arrangement	RGB vertical strip	-
	TFT Controller	ILI6150c / ILI5120 or Compatible	-
	TFT Interface mode	LVDS	-
	Luminance	1000	cd/m2
	Operating temperature	-20.0 ~ +70.0	°C
	Storage temperature	-30.0 ~ +80.0	°C

Note:

- (1) FPC or Wire or Foam Tape or PCBA are not included.
- (2) Color tone is slightly changed by temperature and driving voltage.



T	7	0	0	A	1	6	X	0	0		
---	---	---	---	---	---	---	---	---	---	--	--

TOUCH PANEL  
N : Without Touch Panel  
C : Capacitive Touch Panel - I2C  
D : Capacitive Touch Panel - USB  
R : Resistive Touch Panel

SEMI - CUSTOMISE (MINOR CHANGES FROM STANDARD MODEL)  
00 - STANDARD SPECIFICATION MODEL

OTHER OPTION  
C : STANDARD COVER LENS

OTHER OPTION  
H : WITH HDMI BOARD ATTACHED

REFER TO FACTORY FOR FURTHER INFORMATION.  
TERMS AND CONDITIONS APPLY



## 4. AC/DC Characteristics (TFT)

### 4.1 Absolute Maximum Ratings ( $V_{SS} = 0V$ , $T_a = 25^{\circ}C$ )

Item	Symbol	Min	Typ	Max	Unit	Note
Power Supply Voltage	VDD	-0.3	-	+3.6	V	
Storage Humidity	HST	10	-	90	%RH	(1)
Storage Temperature	TST	-30	-	+80	$^{\circ}C$	
Operating Humidity	HOP	10	-	90	%RH	(1)
Operating Temperature	TOP	-20	-	+70	$^{\circ}C$	

Note:

- (1) At  $25 \pm 5^{\circ}C$ . Absolute humidity shall be less than 90%RH at  $+40^{\circ}C$ .
- (2) Stresses above those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only. Functional operation of this device at these or any other conditions above those indicated in the operational sections of this specification is not implied and exposure to absolute maximum rating conditions for extended periods may affect device reliability.

### 4.2 Electrical Characteristics ( $V_{SS} = 0V$ , $V_{DD} = 3.3V$ , $T_a = 25^{\circ}C$ )

Item	Symbol	Min	Typ	Max	Unit	Note
Power Supply Voltage	VDD	3.0	3.3	3.6	V	(2)
Operation Current	I <sub>DD</sub>	-	28	-	mA	
Analog Supply Voltage	AV <sub>DD</sub>	9.0	9.5	10.1	V	
Analog Supply Current	I <sub>AV<sub>DD</sub></sub>	-	25	-	mA	
Common Voltage	V <sub>COM</sub>	2.7	2.9	3.2	V	
Gate ON Voltage	V <sub>GH</sub>	17	18	19	V	
Gate OFF Voltage	V <sub>GL</sub>	-7	-6	-5	V	
Gate ON Current	I <sub>V<sub>GH</sub></sub>	-	5.5	-	mA	(3)
Gate OFF Current	I <sub>V<sub>GL</sub></sub>	-	8.0	-	mA	(4)
Input Logic High Voltage	V <sub>IH</sub>	0.8V <sub>DD</sub>	-	V <sub>DD</sub>	V	
Input Logic Low Voltage	V <sub>IL</sub>	0	-	0.2V <sub>DD</sub>	V	

Note:

- (1) The recommended operating conditions refer to a range in which operation of this product is guaranteed. Accordingly, please make sure that the module is used within this range. And these current values are measured under the condition that all devices are stopped, each component is stable and logic signal is input.
- (2) The supply voltage is measured and specified at the interface connector of LCM.
- (3)  $V_{GH} = 18.0V$
- (4)  $V_{GL} = -6.0V$



### 4.3 LED Backlight Specification (Ta = 25°C)

Item	Symbol	Condition	Min	Typ	Max	Unit
LED Supply Voltage	VLED	If = 500mA	-	9.0	-	V
LED Supply Current	If		-	500	-	mA
Luminous Intensity	LV	White	900	1000	-	cd/m <sup>2</sup>
Half Life Expectancy	LL	If = 500mA	20000	-	-	hrs
LED Configuration	30 White LED, 3 in Series, 10 in Parallel					

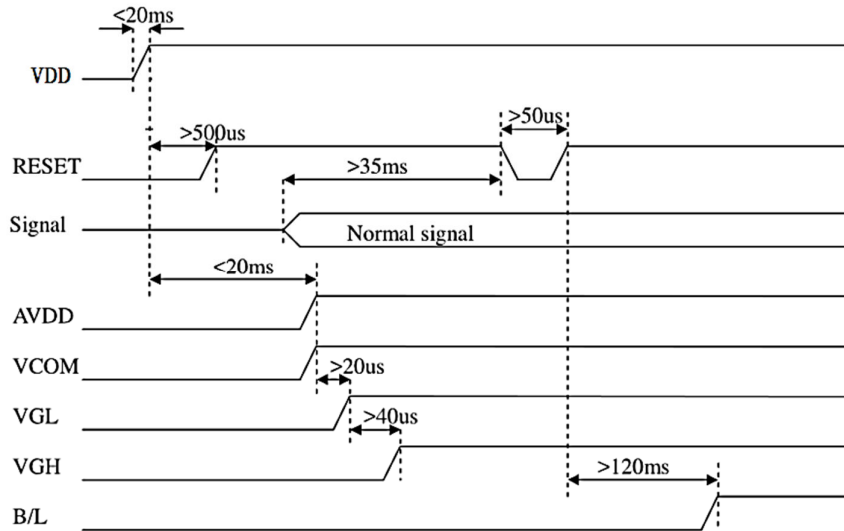
Note:

- (1) The LED Supply Voltage is defined by the number of LED at Ta = 25°C and If = 500mA.
- (2) The “Half Life Expectancy” is defined as the module brightness decrease to 50% original brightness at Ta = 25°C and If = 500mA. The LED lifetime could be decreased if the operating If is larger than 500mA.

### 4.4 Power Sequence

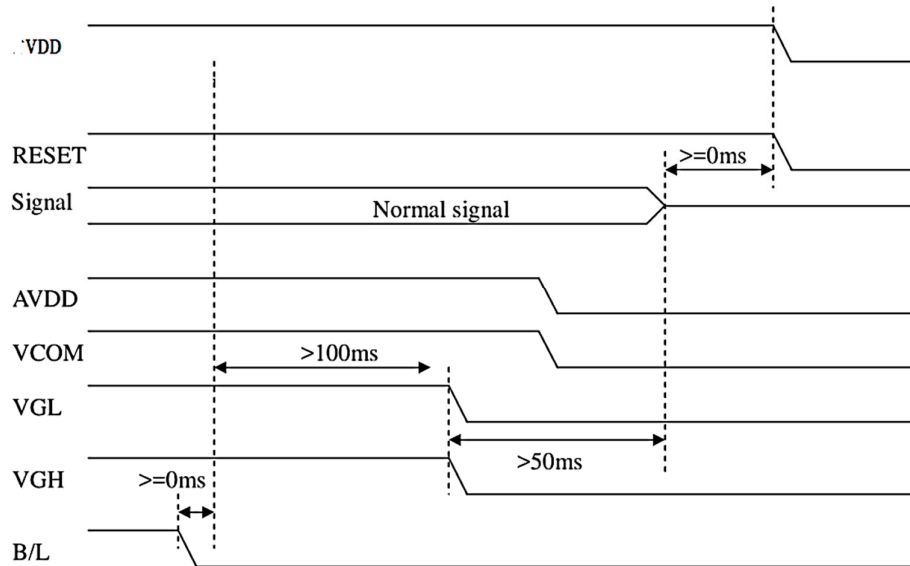
To prevent a latch-up or DC operation of the LCD module, the power ON/OFF sequence shall be as shown in below

#### 4.4.1 Power ON





#### 4.4.2 Power OFF



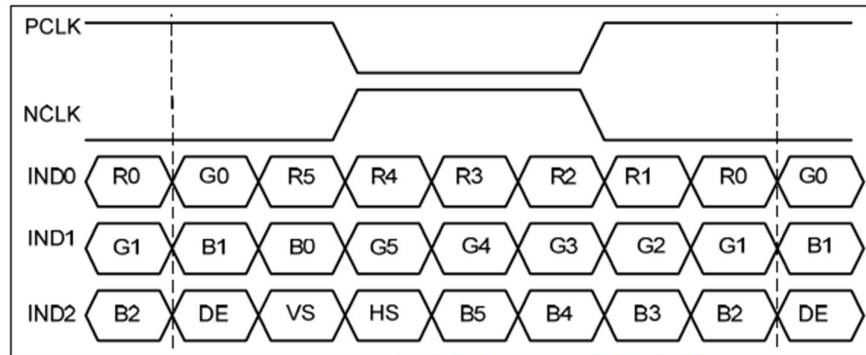
Note:

- (1) When the power supply VDD is 0V, keep the level of input signals on the low or keep high impedance.
- (2) Do not keep the interface signal high impedance when power is on. Back Light must be turn on after power for logic and interface signal are valid.

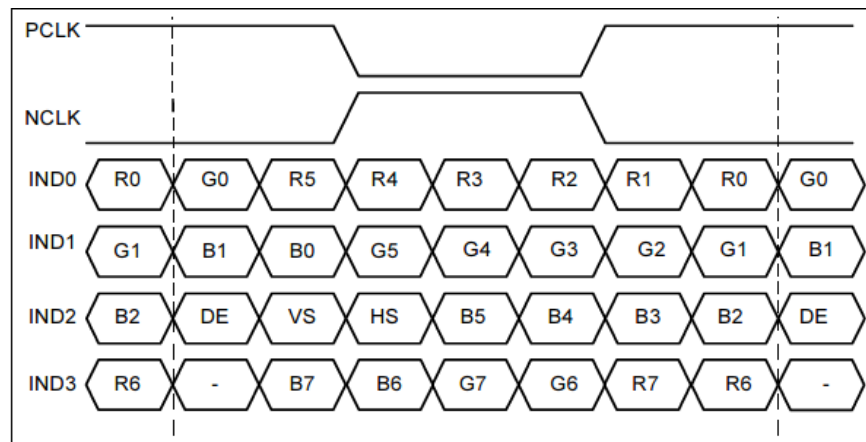




### 4.5 LVDS Interface



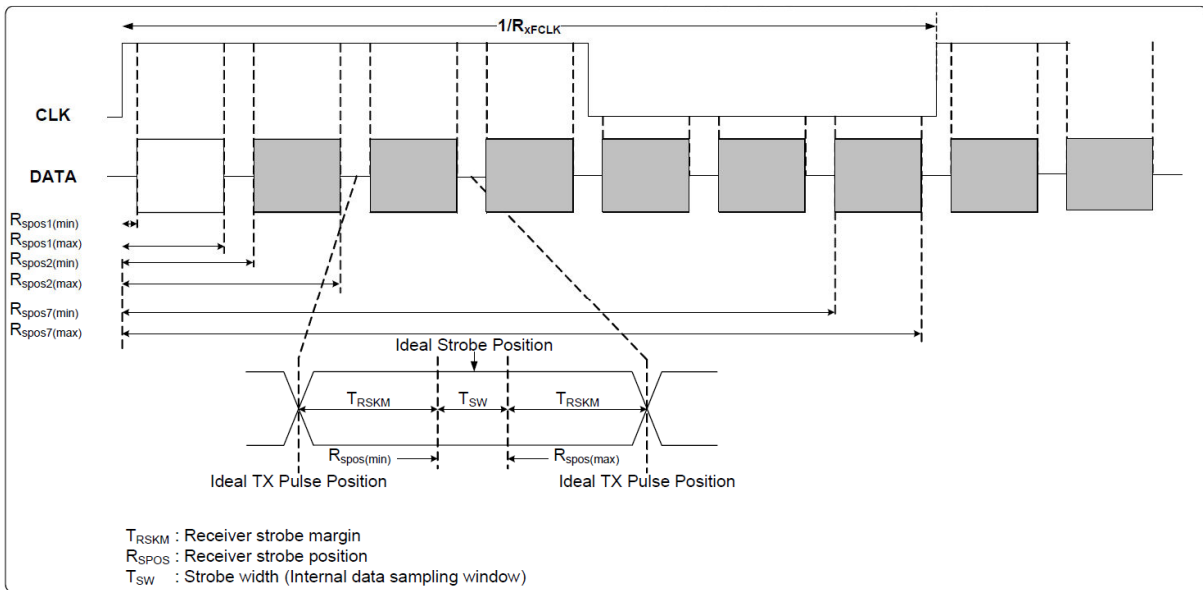
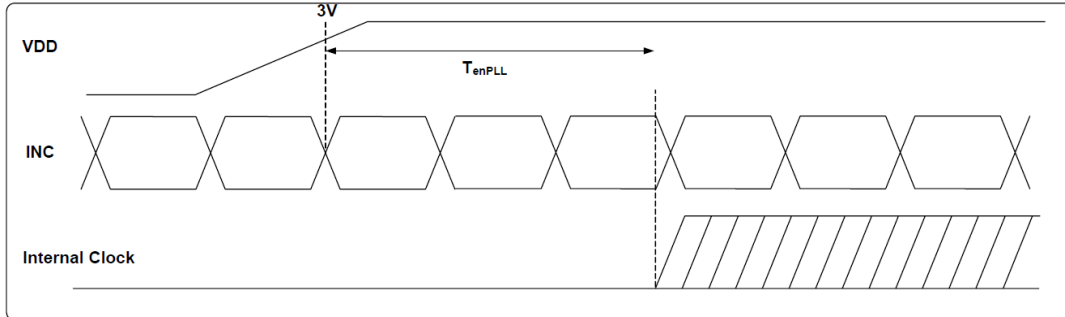
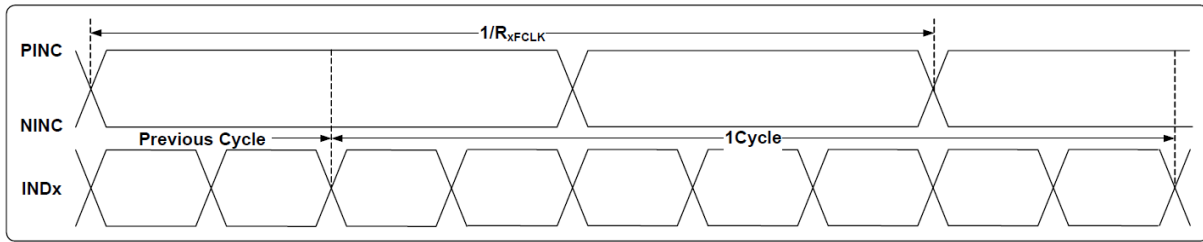
6-bit LVDS Input Timing Chart



8-bit LVDS Input Timing Chart

### 4.6 LVDS Signal Timing

Item	Symbol	Values			Unit	Remarks
		Min.	Typ.	Max.		
Clock Frequency	RxFCLK	20	-	71	MHz	
Input Data Skew Margin	TRSKM	500	-	-	ps	VID  = 400mV RxVCM = 1.2V RxFCLK = 71MHz
Clock High Time	TLVCH		$\frac{4}{7RxFCLK}$		ns	
Clock Low Time	TLVCL		$\frac{3}{7RxFCLK}$		ns	
PLL Wake-up Time	TenPLL			150	µs	





## 4.7 Signal Timing Specification

### 4.7.1 DE Mode

Item	Symbols	Min	Typ	Max	Unit	Note
DCLK Frequency	fclk	40.8	51.2	67.2	MHz	(1)
Horizontal Display Area	t <sub>hd</sub>	1024			DCLK	
HSYNC Period Time	t <sub>h</sub>	1114	1344	1400	DCLK	
HSYNC Blanking	t <sub>hb</sub> + t <sub>hfp</sub>	90	320	376	DCLK	
Vertical Display Area	t <sub>vd</sub>	600			H	
VSYNC Period Time	t <sub>v</sub>	610	635	800	H	
VSYNC Blanking	t <sub>vb</sub> + t <sub>vfp</sub>	10	35	200	H	

Note:

(1) Frame rate = 60Hz

### 4.7.2 HV Mode

#### 4.7.2.1 Horizontal Input Timing

Item	Symbols	Min	Typ	Max	Unit	Note
DCLK Frequency	fclk	44.9	51.2	63	MHz	(1)
Horizontal Display Area	t <sub>hd</sub>	1024			DCLK	
Horizontal Line	t <sub>h</sub>	1200	1344	1400	DCLK	
HSYNC Pulse Width	t <sub>hpw</sub>	1	-	140	DCLK	
HSYNC Back Porch	t <sub>hbp</sub>	160			DCLK	
HSYNC Front Porch	t <sub>hfp</sub>	16	160	216	DCLK	

Note:

(1) Frame rate = 60Hz

#### 4.7.2.2 Vertical Input Timing

Item	Symbols	Min	Typ	Max	Unit	Note
Vertical Display Area	t <sub>vd</sub>	600			H	
VSYNC Period Time	t <sub>v</sub>	624	635	750	H	
VSYNC Pulse Width	t <sub>vpw</sub>	1	-	20	H	
VSYNC Back Porch	t <sub>vbp</sub>	23			H	
VSYNC Front Porch	t <sub>vfp</sub>	1	12	127	H	

**4.8 Pin Assignment Table**

<b>Pin No.</b>	<b>Symbol</b>	<b>I/O</b>	<b>Description</b>	<b>Note</b>
1	VCOM	P	Command Voltage	
2 - 3	VDD	P	Power Supply	
4	NC	-	Not Connected	
5	RESET	I	Global Reset, Active Low	
6	STBYB	I	Standby Mode, H:Normal Operation, L:Standby	
7	GND	P	Ground	
8	RXIN0-	I	LVDS Negative data signal (-)	
9	RXIN0+	I	LVDS Positive data signal (+)	
10	GND	P	Ground	
11	RXIN1-	I	LVDS Negative data signal (-)	
12	RXIN1+	I	LVDS Positive data signal (+)	
13	GND	P	Ground	
14	RXIN2-	I	LVDS Negative data signal (-)	
15	RXIN2+	I	LVDS Positive data signal (+)	
16	GND	P	Ground	
17	RXCLKIN-	I	LVDS Negative CLK signal (-)	
18	RXCLKIN +	I	LVDS Positive CLK signal (+)	
19	GND	P	Ground	
20	RXIN3-	I	LVDS Negative data signal (-)	
21	RXIN3+	I	LVDS Positive data signal (+)	
22	GND	P	Ground	
23 - 24	NC	-	Not Connected	
25	GND	P	Ground	
26 - 27	NC	-	Not Connected	
28	SELB	I	6bit/ 8bit Select, H:6bit, L:8bit	
29	AVDD	P	Power for Analog Circuit	
30	GND	P	Ground	
31 - 32	LED-	P	LED Cathode	
33	L/R	I	Horizontal Inversion	
34	U/D	I	Vertical Inversion	
35	VGL	P	Gate OFF Voltage	
36 - 37	NC	-	Not Connected	
38	VGH	P	Gate ON Voltage	
39 - 40	LED+	P	LED Anode	

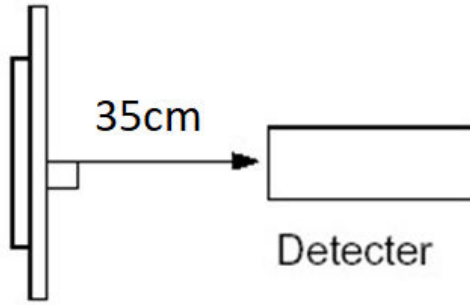


## 5. Optical Characteristics

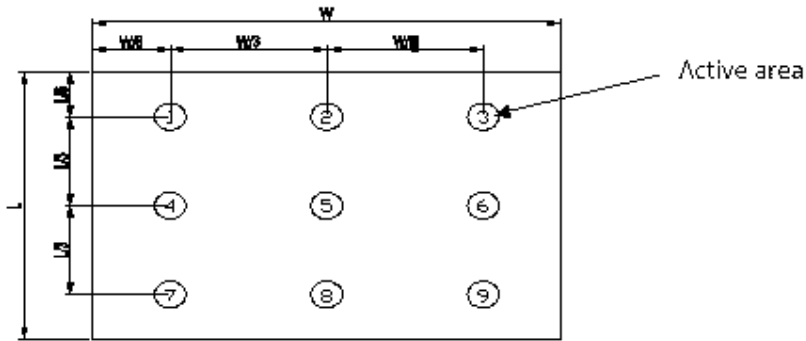
Item	Symbol	Condition	Min	Typ	Max	Unit	Note
Brightness	Bp	$\theta=0^\circ$	900	1000	-	cd/m <sup>2</sup>	(2)
Uniformity	$\Delta Bp$	$\Phi=0^\circ$	80	-	-	%	(2)(3)
Viewing Angle	3:00	$Cr \geq 10$	-	80	-	degree	(4)
	6:00		-	80	-		
	9:00		-	80	-		
	12:00		-	80	-		
Contrast Ratio	Cr		600	800	-	-	(5)
Response Time	Tr		-	10	-	ms	(6)
	Tf		-	10	-	ms	
Colour Chromaticity	Wx	$\theta=0^\circ$ $\Phi=0^\circ$ T=25°C	-	0.28	-	-	(7)
	Wy		-	0.33	-	-	
	Rx		-	0.51	-	-	
	Ry		-	0.34	-	-	
	Gx		-	0.31	-	-	
	Gy		-	0.56	-	-	
	Bx		-	0.15	-	-	
	By		-	0.14	-	-	
NTSC Ratio	S		60	70	-	%	(7)
CTP Transmittance	T		87	90	-	%	(8)

Note:

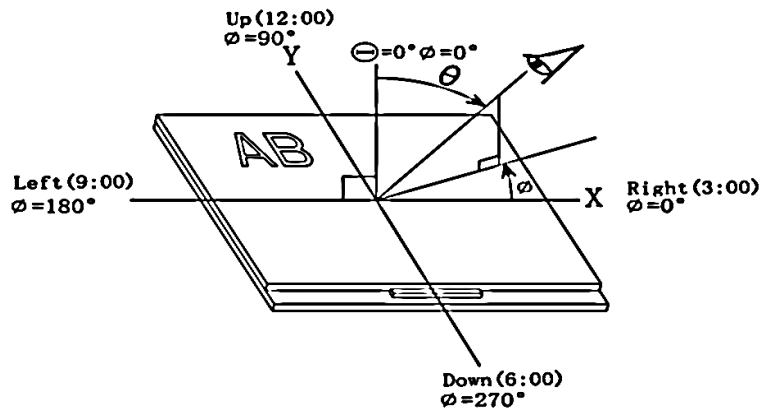
- (1) The parameter is slightly changed by temperature, driving voltage and materiel.
- (2) The data are measured after LEDs are turned on for 5 minutes. LCM displays full white. The brightness is the average value of 9 measured spots. Measurement equipment BM7 ( $\Phi 8mm$ ).  
Measuring condition:
  - Measuring surroundings: Dark room.
  - Measuring temperature: Ta=25°C.
  - Adjust operating voltage to get optimum contrast at the center of the display.
 Measured value at the center point of LCD panel after more than 5 minutes while backlight turning on.



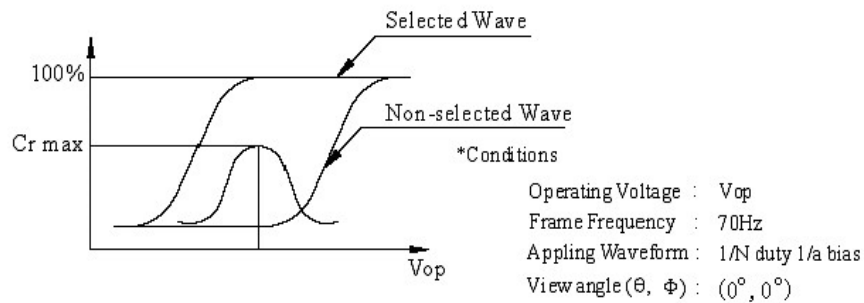
- (3) The luminance uniformity is calculated by using following formula:  
 $\Delta Bp = Bp (\text{Min.}) / Bp (\text{Max.}) \times 100 (\%)$   
 $Bp (\text{Max.}) =$  Maximum brightness in 9 measured spots.  
 $Bp (\text{Min.}) =$  Minimum brightness in 9 measured spots.



- (4) The definition of viewing angle:  
 Refer to the graph below marked by  $\theta$  and  $\phi$



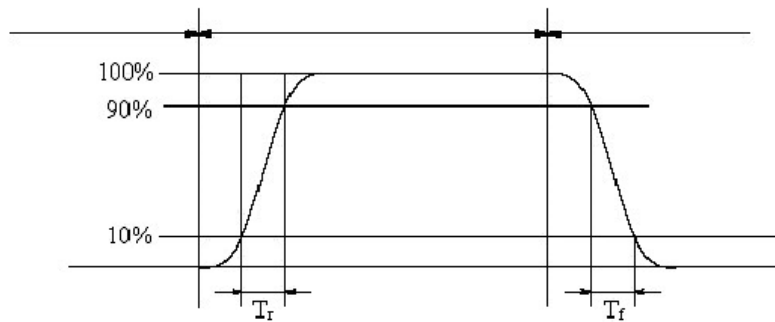
(5) Definition of contrast ratio. (Test LCD using DMS501)



$$\text{Contrast ratio (Cr)} = \frac{\text{Brightness of selected dots}}{\text{Brightness of non-selected dots}}$$

(6) Definition of Response time. (Test LCD using DMS501):

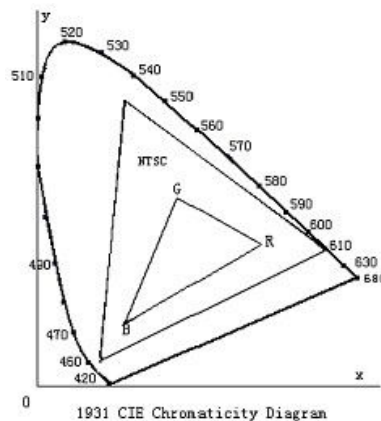
The output signals of photo detector are measured when the input signals are changed from “black” to “white” (falling time) and from “white” to “black” (rising time), respectively. The response time is defined as the time interval between the 10% and 90% of amplitudes. Refer to figure as below.



The Definition of response time

(7) Definition of Color of CIE Coordinate and NTSC Ratio.

Colour coordinates measured at centre point of LCD. All input terminals LCD panel must be ground while measuring the centre area of the panel. The LED driving condition is I<sub>LED</sub>=240mA.





**(8) Definition of The transmittance**

Measurements shall be made at viewing angle of  $\theta = 0^\circ$  and at the center of the touch panel by using BM-7. Transmittance shall be calculated by the difference of the luminance between a standard light source with touch panel and a standard light source without touch panel.

$$\text{Transmittance} = \frac{\text{Luminance on touch panel surface}}{\text{Luminance of standard light source}}$$

**6. Reliability Test Condition**

Item	Test Condition	Note	
Operating	High Temperature	+70°C, 96hrs	(3)
	Low Temperature	-20°C, 96hrs	(3)
	High Temperature and High Humidity	+40°C, 90%RH, 120hrs	(3)
	Cycle	RT (0m) → -20°C (30m) → RT (5m) → +70°C (30m) → RT (5m) 50 cycles	(1)(2)(3)
Storage	High Temperature	+80°C, 240hrs	(3)
	Low Temperature	-30°C, 240hrs	(3)

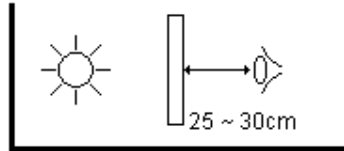
Note:

- (1) RT means Room Temperature.
- (2) m means minute.
- (3) Before cosmetic and functional test, the product must have enough recovery time, at least 2 hours at room temperature.



### 7. Inspection Criteria

- Inspection distance: 25~30cm.
- From lamp source to product: 250 ± 100cm.
- Angle of inspection: Ambient brightness 1k-1.5k lux inspection from front view, perpendicular to the surface of product.



No	Defect	Definition of defect	Inspection Criteria															
1	a) Definition of dot	<p>The size of defective dot over 1/2 of whole is regards as one defective dot.</p> <p>Smaller than 1/2      Larger than 1/2</p> <p>'No dot defect' (ignore)      '1 dot defect' (counted)</p>	<p>A - Viewing area B - Viewing area C - Outside Viewing area</p>															
	b) Bright Dot	Dot appear bright and unchanged in size when LCD panel is displaying black pattern.	<table border="1"> <thead> <tr> <th>Defect</th> <th>A</th> <th>B</th> <th>C</th> </tr> </thead> <tbody> <tr> <td>Bright Dot</td> <td>1</td> <td>1</td> <td rowspan="2">NC</td> </tr> <tr> <td>Dark Dot</td> <td>2</td> <td>2</td> </tr> <tr> <td>Total</td> <td colspan="2">4</td> <td></td> </tr> </tbody> </table> <p>NC – Not Count</p>	Defect	A	B	C	Bright Dot	1	1	NC	Dark Dot	2	2	Total	4		
	Defect	A		B	C													
	Bright Dot	1	1	NC														
Dark Dot	2	2																
Total	4																	
c) Dark Dot	Dot appear dark and unchanged in size when LCD panel is displaying pure color (RED, GREEN or BLUE) pattern.																	
d) 2 dot adjacent	<p>1 pair = 2 dots</p> <p>Type 1      Type 2</p> <p>or</p> <p>Type 3</p>	<table border="1"> <thead> <tr> <th>Defect</th> <th>Acc. Count</th> </tr> </thead> <tbody> <tr> <td>2 Bright dot Adjacent</td> <td>0</td> </tr> <tr> <td>2 Dark dot Adjacent</td> <td>1</td> </tr> </tbody> </table>	Defect	Acc. Count	2 Bright dot Adjacent	0	2 Dark dot Adjacent	1										
Defect	Acc. Count																	
2 Bright dot Adjacent	0																	
2 Dark dot Adjacent	1																	
2	<p>a) Black Spot b) White Spot c) Bright Spot d) Pin Hole e) Foreign Particle</p>	<p>- Black / Dark / Bright Spot is points on display which appear dark/bright and usually result from contamination. - These defect do not vary in size intensity (contrast) when contrast is varied.</p>	<p>A - Viewing area B - Outside Viewing area</p>															



			<table border="1"> <thead> <tr> <th>Defect Category</th> <th>A</th> <th>B</th> </tr> </thead> <tbody> <tr> <td>D &lt; 0.15</td> <td>NC</td> <td rowspan="4">NC</td> </tr> <tr> <td>0.15 ≤ D &lt; 0.25</td> <td>2</td> </tr> <tr> <td>0.25 ≤ D ≤ 0.30</td> <td>1</td> </tr> <tr> <td>D &gt; 0.30</td> <td>0</td> </tr> </tbody> </table>	Defect Category	A	B	D < 0.15	NC	NC	0.15 ≤ D < 0.25	2	0.25 ≤ D ≤ 0.30	1	D > 0.30	0
Defect Category	A	B													
D < 0.15	NC	NC													
0.15 ≤ D < 0.25	2														
0.25 ≤ D ≤ 0.30	1														
D > 0.30	0														
	<ul style="list-style-type: none"> <li>a) Black Line</li> <li>b) White Line</li> <li>c) Particle between POL and Glass</li> <li>d) Scratch on Glass</li> </ul>		<table border="1"> <thead> <tr> <th>Defect Category</th> <th>A</th> <th>B</th> </tr> </thead> <tbody> <tr> <td>W &lt; 0.03</td> <td>NC</td> <td rowspan="4">NC</td> </tr> <tr> <td>0.03 ≤ W ≤ 0.1</td> <td>2</td> </tr> <tr> <td>L ≤ 3.0</td> <td></td> </tr> <tr> <td>W &gt; 0.1</td> <td>0</td> </tr> </tbody> </table>	Defect Category	A	B	W < 0.03	NC	NC	0.03 ≤ W ≤ 0.1	2	L ≤ 3.0		W > 0.1	0
Defect Category	A	B													
W < 0.03	NC	NC													
0.03 ≤ W ≤ 0.1	2														
L ≤ 3.0															
W > 0.1	0														
	<ul style="list-style-type: none"> <li>a) Air Bubble</li> <li>b) POL Bubble</li> <li>c) POL Dented</li> </ul>		<table border="1"> <thead> <tr> <th>Defect Category</th> <th>A</th> <th>B</th> </tr> </thead> <tbody> <tr> <td>D &lt; 0.20</td> <td>NC</td> <td rowspan="4">NC</td> </tr> <tr> <td>0.20 ≤ D &lt; 0.30</td> <td>3</td> </tr> <tr> <td>0.30 ≤ D ≤ 0.50</td> <td>2</td> </tr> <tr> <td>D &gt; 0.50</td> <td>0</td> </tr> </tbody> </table>	Defect Category	A	B	D < 0.20	NC	NC	0.20 ≤ D < 0.30	3	0.30 ≤ D ≤ 0.50	2	D > 0.50	0
Defect Category	A	B													
D < 0.20	NC	NC													
0.20 ≤ D < 0.30	3														
0.30 ≤ D ≤ 0.50	2														
D > 0.50	0														
3	Mura (50% Grey)		Judged by Limit sample												
4	Surface Smudginess	<ul style="list-style-type: none"> <li>- For those that can be cleaned, ≤20% of inspected quantity in one lot under &gt; class 10K area; ≤10% of inspected quantity in one lot ≤ class 10K area.</li> <li>- For those that cannot be cleaned it is classified as foreign round shape defect.</li> </ul>													
5	FPC Defects on Contact Pad		Accept if dent, pinhole:- a) $a \leq w/3$  Reject – if open circuit / cracking / oxidation / contamination												
	FPC Broken		Reject if FPC broken / extruded 												
	FPC Warped		Accept if FPC warped 												



## 8. Precaution

### 1. Handling Precautions

- a. The display panel is made of glass and polarizer. As glass is fragile. It tends to chip during handling especially on the edges. Please avoid dropping or jarring. Do not subject it to a mechanical shock of impact or by dropping it.
- b. If the display panel is damaged and the liquid crystal substance leaks out, be sure not to get any in your mouth. If the substance is in contact with your skin or clothes, wash it off using soap and water.
- c. Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary. Do not touch the display with bare hands. This will stain the display area and degrade the insulation between terminals. Scratch and dents may occur on polarizer too.
- d. The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully. Do not touch, push or rub the exposed polarizers with anything harder than a HB pencil lead (glass, tweezers, etc.). Do not put or attach anything on the display area to avoid leaving marks on it. Condensation on the surface and contact with terminals due to cold will damage, stain or dirty the polarizer. After products are tested at low temperature they must be warmed up in a container before coming in to contact with room temperature air.
- e. If the display surface becomes contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If it is heavily contaminated, moisten cloth with one of the following solvents.
  - Isopropyl alcohol.
  - Ethyl alcohol.
  - Do not scrub hard to avoid damaging the display surface.
- f. Solvents other than those above-mentioned may damage the polarizer. Especially, do not use the following.
  - Water.
  - Ketone.
  - Aromatic solvents.
  - Wipe off saliva or water drops immediately, contact with water over a long period of time may cause deformation or colour fading. Avoid contact with oil and fats.
- g. Exercise care to minimize corrosion of the electrode. Corrosion of the electrodes is accelerated by water droplets, moisture condensation or a current flow in a high-humidity environment.
- h. Install the LCD Module by using the mounting holes. When mounting the LCD module make sure it is free of twisting, warping and distortion. In particular, do not forcibly pull or bend the I/O cable or the backlight cable.
- i. Do not attempt to disassemble or process the LCD module.
- j. NC terminal should be open. Do not connect anything.
- k. If the logic circuit power is off, do not apply the input signals.
- l. Electro-Static Discharge Control. Since this module uses a CMOS LSI, the same careful attention should be paid to electrostatic discharge as for an ordinary CMOS IC. To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.
  - Before removing LCM from its packing case or incorporating it into a set, be sure the module and your body have the same electric potential. Be sure to ground the body when handling the LCD modules.
  - Tools required for assembly, such as soldering irons, must be properly grounded. Make certain the AC power source for the soldering iron does not leak. When using an electric screwdriver to attach LCM, the screw driver should be of ground potentiality to



minimize as much as possible any transmission of electromagnetic waves produced sparks coming from the commutator of the motor.

- To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions. To reduce the generation of static electricity be careful that the air in the work environment is not too dry. A relative humidity of 50%-60% is recommended. As far as possible make the electric potential of your work clothes and that of the work bench the ground potential.
- The LCD module is coated with a film to protect the display surface. Exercise care when peeling off this protective film since static electricity may be generated.
- m. Since LCM has been assembled and adjusted with a high degree of precision, avoid applying excessive shocks to the module or making any alterations or modifications to it.
  - Do not alter, modify or change the shape of the tab on the metal frame.
  - Do not make extra holes on the printed circuit board, modify its shape or change the positions of components to be attached.
  - Do not damage or modify the pattern writing on the printed circuit board.
  - Absolutely do not modify the zebra rubber strip (conductive rubber) or heat seal connector.
  - Except for soldering the interface, do not make any alterations or modifications with a soldering iron.
  - Do not drop, bend or twist the LCM.

## 2. Storage Precautions

When storing the LCD modules, the following precaution are necessary.

- a. Store them in a sealed polyethylene bag. If properly sealed, there is no need for the desiccant.
- b. Store them in a dark place. Do not expose to sunlight or fluorescent light, keep the temperature between 0°C and 35°C, and keep the relative humidity between 40%RH and 60%RH.
- c. The polarizer surface should not come in contact with any other objects.

## 3. Others

- a. Liquid crystals solidify under low temperature (below the storage temperature range) leading to defective orientation or the generation of air bubbles (black or white). Air bubbles may also be generated if the module is subject to a low temperature.
- b. If the LCD modules have been operating for a long time showing the same display patterns, the display patterns may remain on the screen as ghost images and a slight contrast irregularity may also appear. A normal operating status can be regained by suspending use for some time. It should be noted that this phenomenon does not adversely affect performance reliability.
- c. To minimize the performance degradation of the LCD modules resulting from destruction caused by static electricity etc. Exercise care to avoid holding the following sections when handling the modules.
  - Exposed area of the printed circuit board.
  - Terminal electrode sections.

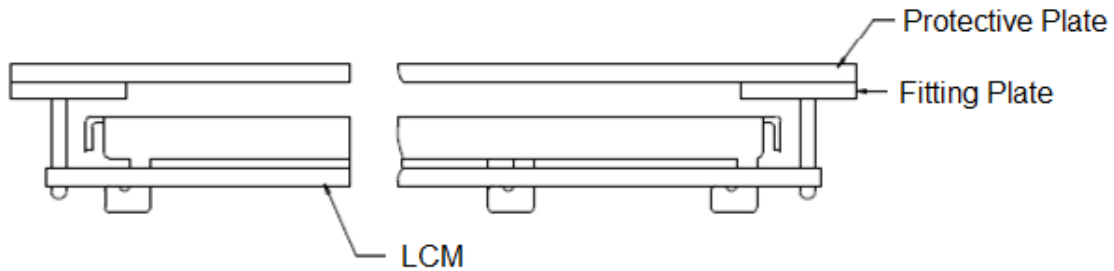
## 4. Using LCD Modules

### a. Installing LCD Modules

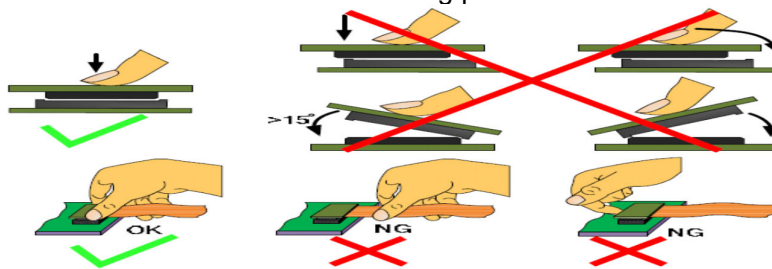
The hole in the printed circuit board is used to fix LCM as shown in the picture below.

Attend to the following items when installing the LCM.

- b. Cover the surface with a transparent protective plate to protect the polarizer and LC cell.



- c. When assembling the LCM into other equipment, the spacer to the bit between the LCM and the fitting plate should have enough height to avoid causing stress to the module surface, refer to the individual specifications for measurements. The measurement tolerance should be 0.1mm.
- d. Precaution for assemble the module with BTB connector:  
Please note the position of the male and female connector position, don't assemble or assemble like the method which the following picture shows.



5. Precaution for soldering the LCM

	Manual soldering	Machine drag soldering	Machine press soldering
No RoHS Product	290°C ~350°C. Time: 3-5S.	330°C ~350°C. Speed: 4-8 mm/s.	300°C ~330°C. Time: 3-6S. Press: 0.8~1.2Mpa
RoHS Product	340°C ~370°C. Time: 3-5S.	350°C ~370°C. Time: 4-8 mm/s.	330°C ~360°C. Time: 3-6S. Press: 0.8~1.2Mpa

- a. If soldering flux is used, be sure to remove any remaining flux after finishing the soldering operation (This does not apply in the case of a non-halogen type of flux). It is recommended that you protect the LCD surface with a cover during soldering to prevent any damage due to flux spatters.
- b. When soldering the electroluminescent panel and PC board, the panel and board should not be detached more than three times. This maximum number is determined by the temperature and time conditions mentioned above, though there may be some variance depending on the temperature of the soldering iron.
- c. When removing the electroluminescent panel from the PC board, be sure the solder has completely melted, the soldered pad on the PC board could be damaged.

6. Precautions for Operation

- a. Viewing angle varies with the change of liquid crystal driving voltage (VLCD). Adjust VLCD to show the best contrast.
- b. It is recommended to drive LCD's within the specified voltage limit since over limit will cause shorter LCD life. An electrochemical reaction due to direct current causes LCD deterioration. Avoid the use of direct current drive.
- c. Response time will be extremely delayed at lower temperature compared to room operating temperature range and on the other hand, at higher temperature LCD-shows dark color in



them. However those phenomena do not mean malfunction. The LCD will return to normal performance when ambient temperature revert to room condition.

- d. If the display area is pushed hard during operation, the display will become abnormal. However, it will return to normal if it is turned off and on.
- e. A slight dew depositing on terminals is a cause for electro-chemical reaction resulting in terminal open circuit.
- f. Input logic voltage before apply analogue high voltage such as LCD driving voltage when power on. Remove analogue high voltage before logic voltage when power off the module. Input each signal after the positive/negative voltage becomes stable.
- g. Please keep the temperature within the specified range for use and storage. Polarization degradation, bubble generation or polarizer peel-off may occur with high temperature and high humidity.

**7. Safety**

- a. It is recommended to crush damaged or unnecessary LCDs into pieces and wash them off with solvents such as acetone and ethanol, which should later be burned.
- b. If any liquid leaks out of a damaged glass cell and comes in contact with the hands, wash off thoroughly with soap and water.

**8. Limited Warranty**

Unless otherwise agreed between Crystal Clear Technology and customer, Crystal Clear Technology will replace or repair any of its LCD and LCM which is found to be defective electrically and visually when inspected in accordance with Crystal Clear Technology acceptance standards, for a period of one year from date of shipment. Confirmation of such date shall be based on freight documents. The warranty liability of Crystal Clear Technology is limited to repair and/or replacement on the terms set forth above. Crystal Clear Technology will not responsible for any subsequent or consequential events.

**9. Return LCM under Warranty**

No warranty can be granted if the precautions stated above have been disregarded. The typical examples of violations are:

- Broken LCD glass.
- PCB eyelet's damaged or modified.
- PCB conductors damaged.
- Circuit modified in any way, including addition of components.
- PCB tampered with by grinding, engraving or painting varnish.
- Soldering to, or modifying the bezel in any manner.

Module repairs will be invoiced to customer upon mutual agreement. Modules must be returned with sufficient description of failure or defects. Any connectors or cable installed by customer must be removed completely without damaging the PCB eyelet's, conductors and terminals.



### 9. Mechanical Specification

