

CRYSTAL CLEAR TECHNOLOGY

Product Specification

T200A08X00

(REVISION1)

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2.0 Records of Revision

Rev	Date	Item	Page	Comment	Originator	Checked By
1.0	07/05/18			Initial Release	SCChong	Azhar



3.0 General Specification

T200A08X00 is 2.0" color TFT-LCD (Thin Film Transistor Liquid Crystal Display) module composed of LCD panel, driver ICs control circuit and LED backlight. This product compliant with RoHS environmental requirement.

Item	Contents	Unit	Note
LCD Type	2.0" TFT	-	
Display color	262K		1
Viewing Direction (Optimum View)	All	O 'Clock	
Module size	35.80 x 51.20 x 2.13	mm	2
Active Area(W×H)	30.60 x 40.70	mm	
Number of Dots	240(RGB)×320	dots	
Contrast Ratio	800 : 1	-	
Controller	ST7789V	-	
Backlight	3 White LEDs	pcs	
Brightness	250	cd/m2	
Interface Mode	MCU 8-bit / 3-SPI / 4 SPI	-	
Surface Finishing	Hard Coating	-	

Note1: Color tone is slightly changed by temperature and driving voltage.

Note2: FPC or wire are not included.

Note3: Brightness on LCD surface. Module with CTP or RTP, brightness will be about 20% (max) lower on the touch panel surface.

AVAILABLE OPTION

<input type="checkbox"/> <input checked="" type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input checked="" type="checkbox"/>
<p>TOUCH PANEL N : Without Touch Panel C : Capacitive Touch Panel R : Resistive Touch Panel</p> <p>SEMI - CUSTOMISE (MINOR CHANGES FROM STANDARD MODEL) 00 - STANDARD SPECIFICATION MODEL</p> <p>OTHER OPTION C : STANDARD COVER LENS</p> <p>REFER TO FACTORY FOR FURTHER INFORMATION. TERMS AND CONDITIONS APPLY</p>



4.0 Absolute Maximum Ratings

4.1 Electrical Absolute Maximum ratings (V_{ss} = 0V, T_a = 25°C)

Item	Symbol	Min.	Max.	Unit	Note
Power Supply	VDD	-0.3	4.6	V	1, 2
Digital Input Voltage	VDDIO	-0.3	4.6	V	
Humidity	-	-	90	%RH	

Notes:

1. If the module is above these absolute maximum ratings. It may become permanently damaged.
2. V_{CC} > V_{SS} must be maintained.
3. Please be sure users are grounded when handing LCD Module.

4.2 Environmental Absolute Maximum Ratings

Item	Storage		Operating		Note
	MIN.	MAX.	MIN.	MAX.	
Ambient Temperature	-30°C	80°C	-20°C	70°C	1,2
Humidity	-	-	-	-	3

1. The response time will become lower when operated at low temperature.
2. Background color changes slightly depending on ambient temperature. The phenomenon is reversible.
3. T_a ≤ 40°C and 85%RH MAX.
(T_a ≥ 40°C. Absolute humidity must be lower than the humidity of 85%RH at 40°C)

**5.0 Electrical Characteristics and Instruction Code****5.1 Electrical Characteristics (V_{SS} = 0V, T_a = 25°C)**

Parameter	Symbol	Min	Typ	Max	Unit	Note
Supply Voltage	VDD	2.6	2.8	3.3	V	
Interface Operation Voltage	VDDIO	1.65	1.8	3.3	V	
Digital Input Voltage	H level	V _{IH}	0.7VDDIO	-	VDDIO	V
	L level	V _{IL}	VSS	-	0.3VDDIO	V
Digital output Voltage	H level	V _{OH}	0.8VDDIO	-	VDDIO	V
	L level	V _{OL}	VSS	-	0.2VDDIO	V
Current Supply	IDD	-	7.4	8.0	mA	Note 1

Note:

1. Interface Operation voltage (VDDIO) equal to the Supply Voltage (VDD = 3.3V)

5.2 LED Backlight Specification (V_{SS} = 0V, T_a = 25°C)

Item	Symbol	Condition	Min	Typ	Max	Unit	Note
Supply Voltage	V _{LED}	-	3.0	3.2	3.4	V	1
Supply Current	I _f	-	-	45	-	mA	2
Led lifetime			30000				3

Note:

1. $V_{LED} = V_{LED (+)} - V_{LED (-)}$.
2. It is recommended that customer supply constant current to prolong the led lifetime and optimum led performance
3. Definition of Lifetime: Luminance < 50% of initial Luminance
(Test condition: T_a = 25°C, Constant current supply (typical Value))

**5.3 Interface Signal**

Pin No.	Symbol	I/O	Function
1	NC		No Connection
2	VLED-	P	LED back light(Cathode)
3	VLED+	P	LED back light(Anode)
4	IM0	I	The MCU interface mode select
5	IM1	I	The MCU interface mode select
6	IM2	I	The MCU interface mode select
7	VDD	P	Power Supply
8	VDDIO	P	Interface Operating Voltage
9	TE	O	Tearing effect signal is used to synchronize MCU to frame memory
10	SDA	I/O	SPI interface input/output pin If not used, please fix this pins at VDDIO or GND
11	CS	I	Chip select pin
12	RESET	I	Reset pin
13	RS/SCL	I	Display data/command selection pin in parallel interface Used to be serial interface clock
14	WR	I	Write enable in MCU parallel interface If not used, please fix this pins at VDDIO or GND
15	RD	I	Read enable in parallel interface If not used, please fix this pins at VDDIO or GND
16 -23	D7 – D0	I/O	DB[7:0] are used as MCU parallel interface data bus If not used, please fix this pins at VDDIO or GND
24	GND	P	Ground
25	XR/NC	O	For Resistive Touch panel only
26	YD/NC	O	
27	XL/NC	O	
28	YU/NC	O	

The MCU interface mode select:

IM2	IM1	IM0	MCU Interface Mode
0	0	0	8080 8-bit parallel
1	0	1	3-line 9-bit
1	1	0	4-line 8-bit



6.0 Optical Characteristics

The following items are measured under stable conditions. The optical characteristics should be measured in dark room or equivalent state with the methods shown in Note 1, Note2 and Note3.

Item	Symbol	Condition	Min	Typ	Max	Unit	Remark	
Response time	$T_R + T_F$	$\Theta=0$	-	30	40	ms	Note 4,6	
Contrast ratio	CR	At optimized viewing angle	640	800	-	-	Note 5,6	
Viewing Angle	Hor.	Θ_R	$CR \geq 10$	-	80	-	Degree	Note 6,7
		Θ_L		-	80	-		
	Ver.	Θ_U		-	80	-		
		Θ_D		-	80	-		
White Chromaticity shift	X	$\Theta=0$	-	0.296	-	-		
	Y		-	0.325	-			
Brightness		$\Theta=0$	200	250				

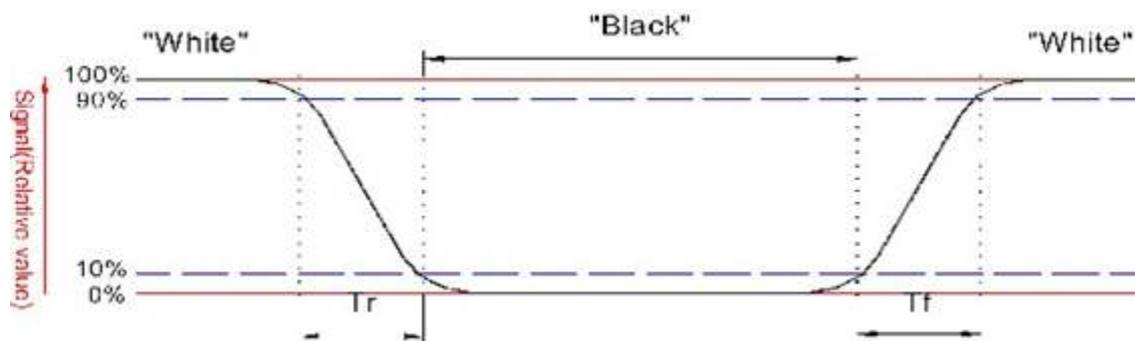
Note1: Ambient temperature=25°C

Note2: To be measured in the dark room.

Note3: To be measured on the center area of panel with a field angle of 1°C by Topcon luminance meter BM-7, after 10 minutes operation.

Note4: Definition of response time:

The output signals of photo detector are measured when the input signals are changed from “black” to “white” (falling time) and from “white” to “black” (rising time), respectively. The response time is defined as the time interval between the 10% and 90% of amplitudes. Refer to figure as below.



Note5: Definition of contrast ratio:



Contrast ratio is calculated with the following formula.

$$\text{Contrast ratio (CR)} = \frac{\text{Brightness measured when LCD is at "white state"}}{\text{Brightness measured when LCD is at "black state"}}$$

Note6: White $V_i = \overline{Vi50} + 1.5V$

Black $V_i = Vi50 \pm 2.0V$

"±" means that the analog input signal swings in phase with COM signal.

"+" means that the analog input signal swings out of phase with COM signal.

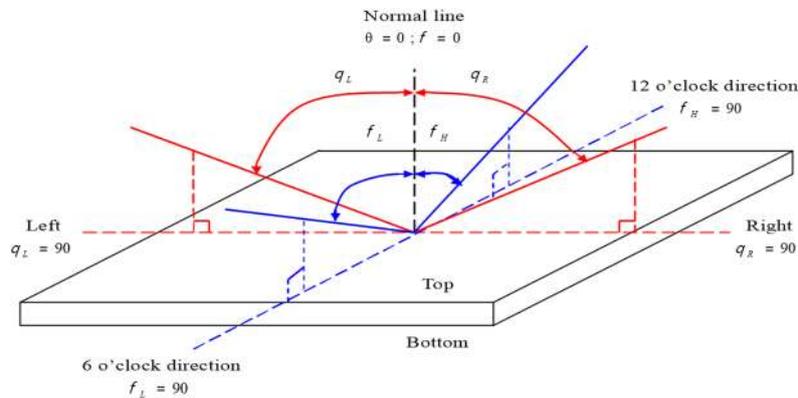
Vi50: The analog input voltage when transmission is 50%

The 100% transmission is defined as the transmission of LCD panel when all

The input terminals of module are electrically opened.

Note7: Definition of viewing angle:

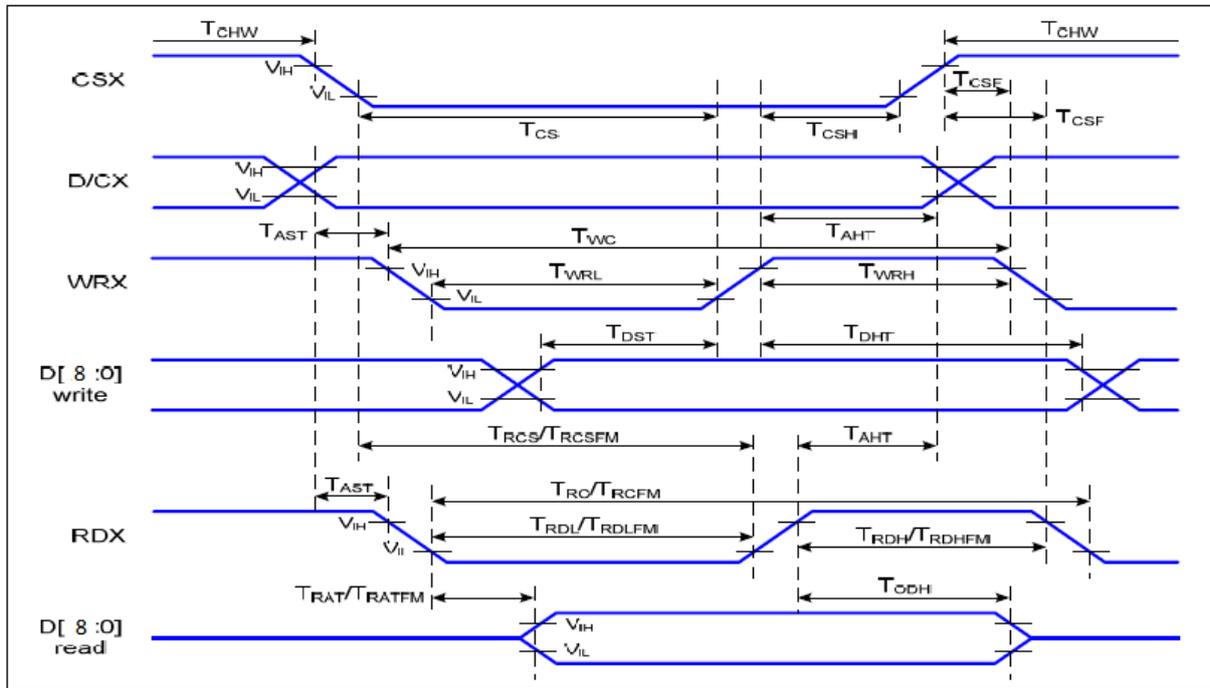
Refer to figure as below.





7.0 AC Characteristics

7.1 8080 Series MCU Parallel Interface Characteristics: 8-bit Bus



VDDI=1.65 to 3.3V, VDD=2.4 to 3.3V, AGND=DGND=0V, Ta= -30 to 70 °C

Signal	Symbol	Parameter	Min	Max	Unit	Description
D/CX	T _{AST}	Address setup time	0		ns	-
	T _{AHT}	Address hold time (Write/Read)	10		ns	
CSX	T _{CHW}	Chip select "H" pulse width	0		ns	-
	T _{CS}	Chip select setup time (Write)	15		ns	
	T _{RCS}	Chip select setup time (Read ID)	45		ns	
	T _{RCSFM}	Chip select setup time (Read FM)	355		ns	
	T _{CSF}	Chip select wait time (Write/Read)	10		ns	
	T _{CSH}	Chip select hold time	10		ns	
WRX	T _{WC}	Write cycle	66		ns	-
	T _{WRH}	Control pulse "H" duration	15		ns	
	T _{WRL}	Control pulse "L" duration	15		ns	
RDX (ID)	T _{RC}	Read cycle (ID)	160		ns	When read ID data
	T _{RDH}	Control pulse "H" duration (ID)	90		ns	
	T _{RDL}	Control pulse "L" duration (ID)	45		ns	
RDX (FM)	T _{RCFM}	Read cycle (FM)	450		ns	When read from frame memory
	T _{RDHFM}	Control pulse "H" duration (FM)	90		ns	
	T _{RDLFM}	Control pulse "L" duration (FM)	355		ns	
D[8:0]	T _{DST}	Data setup time	10		ns	For CL=30pF



	T_{DHT}	Data hold time	10		ns
	T_{RAT}	Read access time (ID)		40	ns
	T_{RATFM}	Read access time (FM)		340	ns
	T_{ODH}	Output disable time	20	80	ns

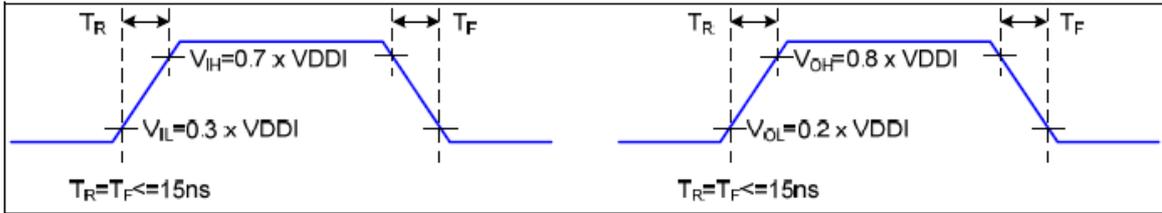


Figure Rising and Falling Timing for I/O Signal

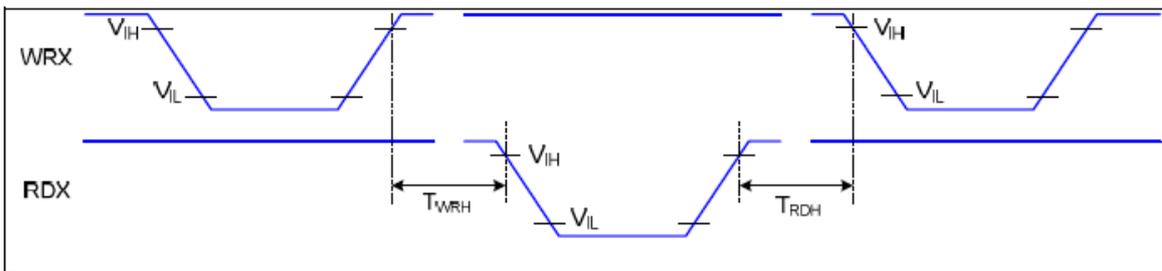
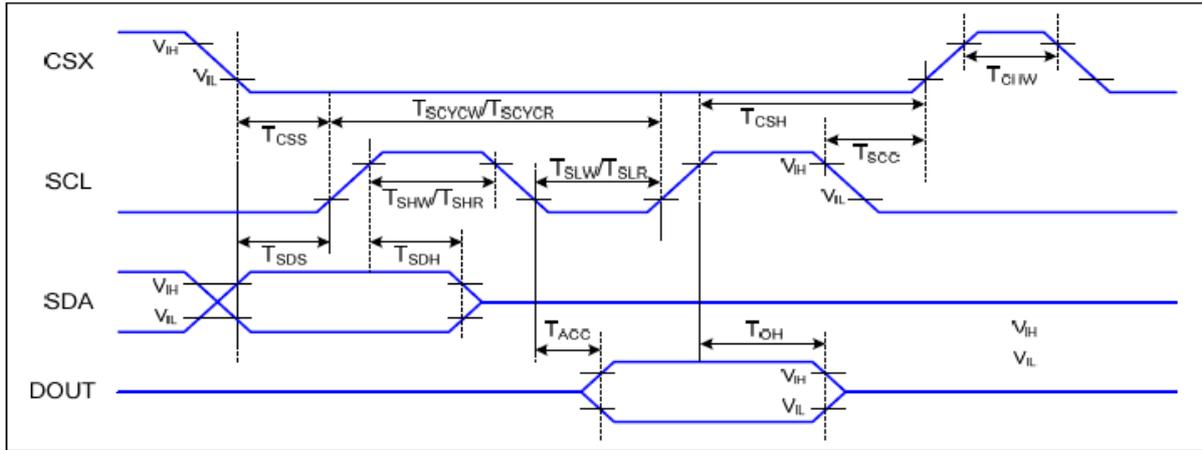


Figure Write-to-Read and Read-to-Write Timing

Note: The rising time and falling time (T_r , T_f) of input signal and fall time are specified at 15 ns or less. Logic high and low levels are specified as 30% and 70% of VDDI for Input signals.



7.2 Serial Interface Characteristics (3-line serial)



VDDI=1.65 to 3.3V, VDD=2.4 to 3.3V, AGND=DGND=0V, Ta=-30 to 70 °C

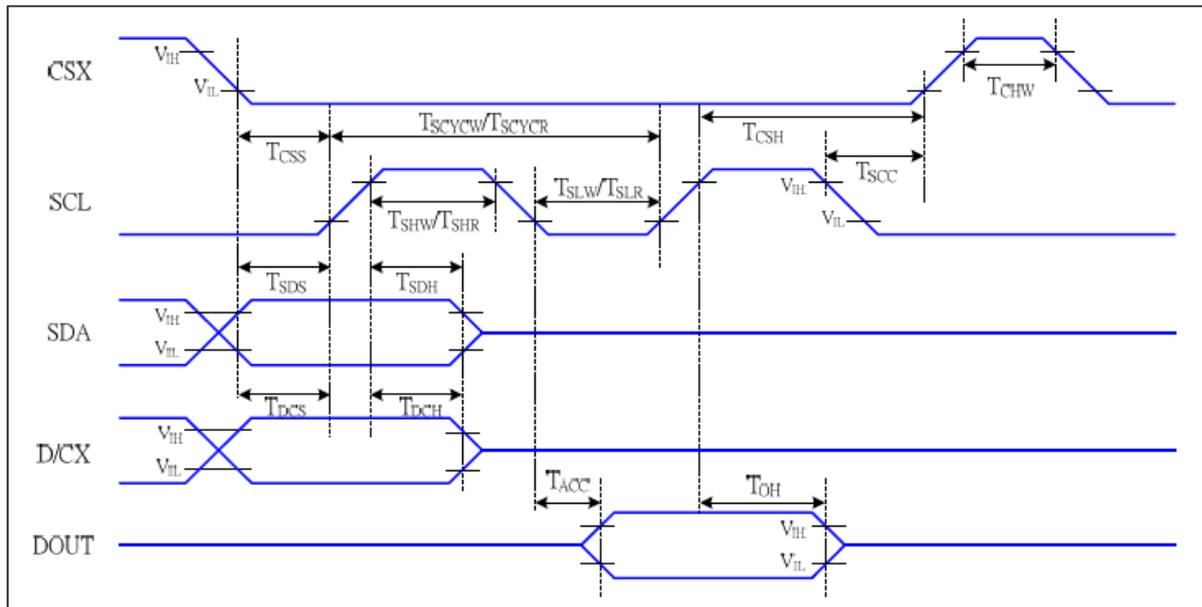
Signal	Symbol	Parameter	Min	Max	Unit	Description
CSX	T _{CSS}	Chip select setup time (write)	15		ns	
	T _{CSH}	Chip select hold time (write)	15		ns	
	T _{CSS}	Chip select setup time (read)	60		ns	
	T _{SCC}	Chip select hold time (read)	65		ns	
	T _{CHW}	Chip select "H" pulse width	40		ns	
SCL	T _{SCYCW}	Serial clock cycle (Write)	66		ns	
	T _{SHW}	SCL "H" pulse width (Write)	15		ns	
	T _{SLW}	SCL "L" pulse width (Write)	15		ns	
	T _{SCYCR}	Serial clock cycle (Read)	150		ns	
	T _{SHR}	SCL "H" pulse width (Read)	60		ns	
	T _{SLR}	SCL "L" pulse width (Read)	60		ns	
SDA (DIN)	T _{SDS}	Data setup time	10		ns	
	T _{SDH}	Data hold time	10		ns	
DOUT	T _{ACC}	Access time	10	50	ns	For maximum CL=30pF
	T _{OH}	Output disable time	15	50	ns	For minimum CL=8pF

Table 5 3-line serial Interface Characteristics

Note : The rising time and falling time (Tr, Tf) of input signal are specified at 15 ns or less. Logic high and low levels are specified as 30% and 70% of VDDI for Input signals.



7.3 Serial Interface Characteristics (4-line serial)

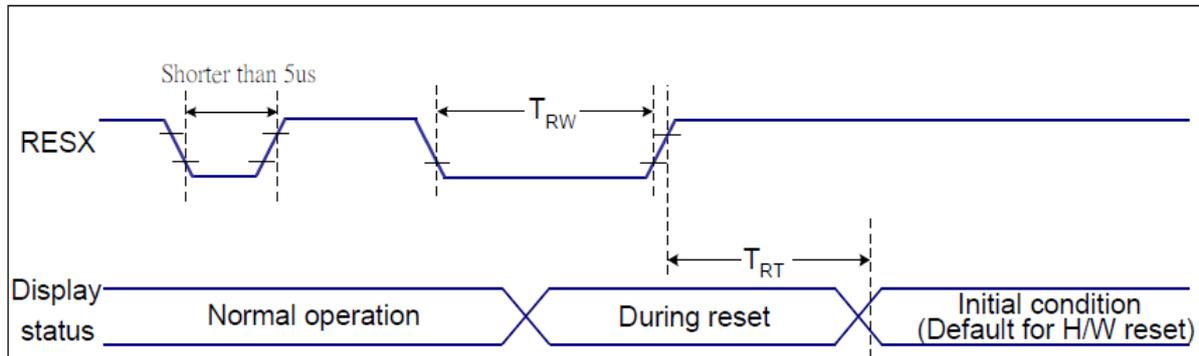


VDDI=1.65 to 3.3V, VDD=2.4 to 3.3V, AGND=DGND=0V, Ta=-30 to 70 °C

Signal	Symbol	Parameter	MIN	MAX	Unit	Description
CSX	T _{CSS}	Chip select setup time (write)	15		ns	
	T _{CSH}	Chip select hold time (write)	15		ns	
	T _{CSS}	Chip select setup time (read)	60		ns	
	T _{SCC}	Chip select hold time (read)	65		ns	
	T _{CHW}	Chip select "H" pulse width	40		ns	
SCL	T _{SCYCW}	Serial clock cycle (Write)	66		ns	-write command & data ram
	T _{SHW}	SCL "H" pulse width (Write)	15		ns	
	T _{SLW}	SCL "L" pulse width (Write)	15		ns	
	T _{SCYCR}	Serial clock cycle (Read)	150		ns	-read command & data ram
	T _{SHR}	SCL "H" pulse width (Read)	60		ns	
	T _{SLR}	SCL "L" pulse width (Read)	60		ns	
D/CX	T _{DCS}	D/CX setup time	10		ns	
	T _{DCH}	D/CX hold time	10		ns	
SDA (DIN)	T _{SDS}	Data setup time	10		ns	
	T _{SDH}	Data hold time	10		ns	
DOUT	T _{ACC}	Access time	10	50	ns	For maximum CL=30pF
	T _{OH}	Output disable time	15	50	ns	For minimum CL=8pF



7.4 Reset Timing



VDDI=1.65 to 3.3V, VDD=2.4 to 3.3V, AGND=DGND=0V, Ta=30 ~ 70 °C

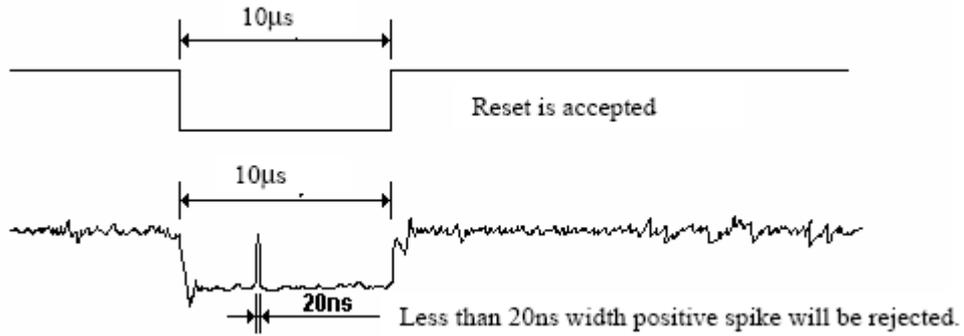
Related Pins	Symbol	Parameter	MIN	MAX	Unit
RESX	TRW	Reset pulse duration	10	-	us
	TRT	Reset cancel	-	5 (Note 1, 5)	ms
			120 (Note 1, 6, 7)	ms	

Notes:

1. The reset cancel includes also required time for loading ID bytes, VCOM setting and other setting from NVM (or similar device) to registers. This loading is done every time when there is HW reset cancel time (tRT) within 5ms after arising edge of RESX.
2. Spike due to an electrostatic discharge on RESX line does not cause irregular system reset according to the table below:

RESX Pulse	Action
Shorter than 5us	Reset Rejected
Longer than 9us	Reset
Between 5us and 9us	Reset starts

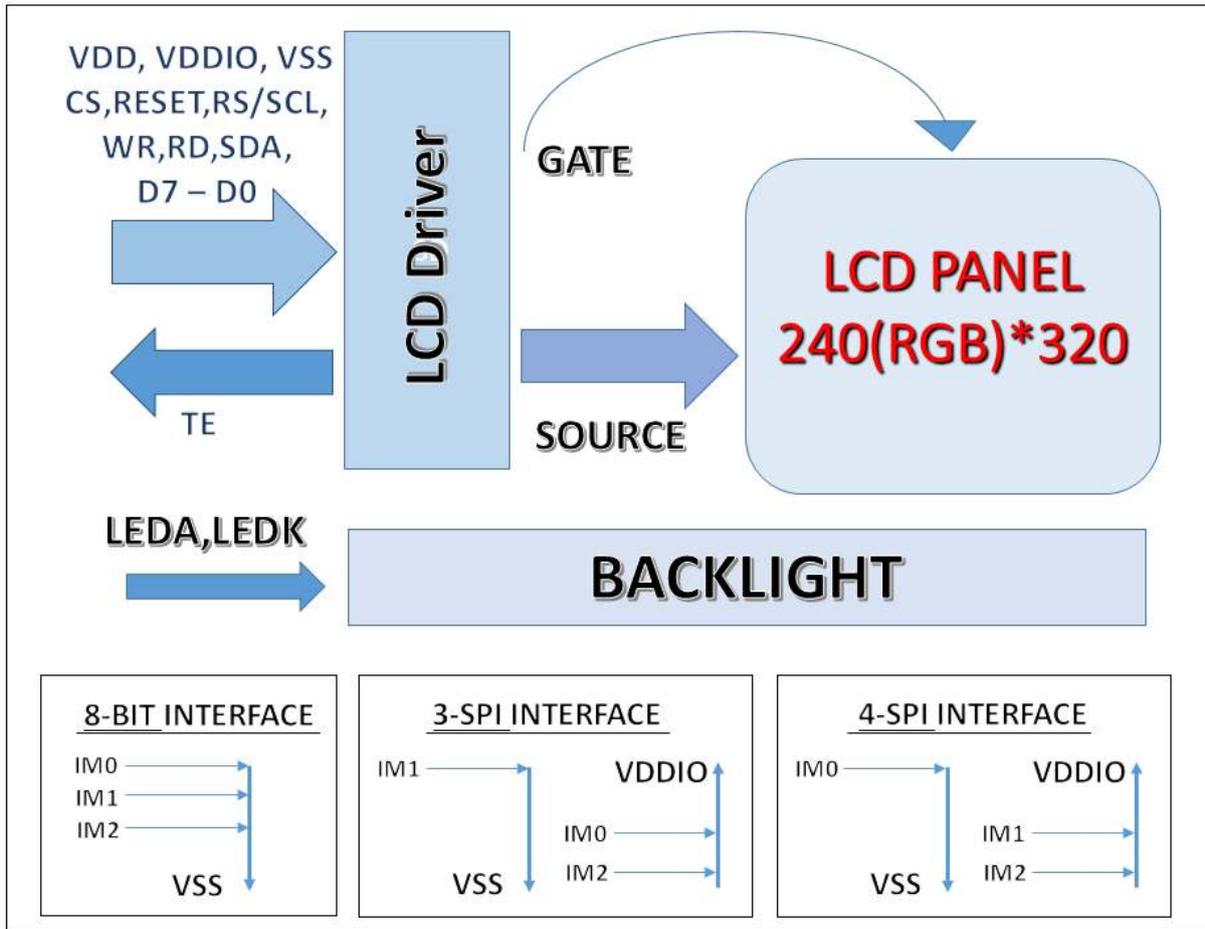
3. During the Resetting period, the display will be blanked (The display is entering blanking sequence, which maximum time is 120ms, when Reset Starts in Sleep Out-mode. The display remains the blank state in Sleep In-mode.) and then return to default condition for Hardware Reset.
4. Spike Rejection also applies during a valid reset pulses as shown below:



5. When Reset applied during Sleep In Mode.
6. When Reset applied during Sleep Out Mode.
7. It is necessary to wait 5ms after releasing RESX before sending commands.
Also Sleep Out command cannot be sent for 120ms.



8.0 Block Diagram





9.0 Reliability Test Condition

Item		Test Condition
Operating	High Temperature	70degC, 240 hrs
	Low Temperature	-20degC,240 hrs
Storage	High Temperature	80degC, 240hrs and recovery for 2hrs
	Low Temperature	-30degC, 240hrs and recovery for 2hrs
	High Temperature and High Humidity	50degC, 90%RH, 240hrs and recovery for 2 hrs
Thermal	Cycle	RT → -30degC → Rt → 80degC → RT 0min 30min 5min 30min 5min 50 cycles (Power off)
	Shock	RT → -30degV → 80degC 0min 30min 30min 50 cycles (Power off)

Note: RT means Room temperature



10.0 Inspection Criteria

No	Defect	Definition of defect	Inspection Criteria												
1	a) Definition of dot	<p>The size of defective dot over 1/2 of whole is regards as one defective dot.</p> <p>Smaller than 1/2 Larger than 1/2</p> <p>R G B R G B</p> <p>'No dot defect' (ignore) '1 dot defect' (counted)</p>	<p>A – Viewing Area B – Outside viewing area</p>												
	b) Bright Dot	Dot appear bright and unchanged in size when LCD panel is displaying black pattern	<table border="1"> <thead> <tr> <th>Defect</th> <th>A</th> <th>B</th> </tr> </thead> <tbody> <tr> <td>Bright Dot</td> <td>1</td> <td rowspan="2">NC</td> </tr> <tr> <td>Dark Dot</td> <td>2</td> </tr> <tr> <td>Total</td> <td>3</td> <td></td> </tr> </tbody> </table>	Defect	A	B	Bright Dot	1	NC	Dark Dot	2	Total	3		
	Defect	A	B												
	Bright Dot	1	NC												
Dark Dot	2														
Total	3														
c) Dark Dot	Dot appear dark and unchanged in size when LCD panel is displaying pure color (RED, GREEN or BLUE) pattern	<table border="1"> <thead> <tr> <th>Defect</th> <th>A</th> <th>B</th> </tr> </thead> <tbody> <tr> <td>Bright Dot</td> <td>1</td> <td rowspan="2">NC</td> </tr> <tr> <td>Dark Dot</td> <td>2</td> </tr> <tr> <td>Total</td> <td>3</td> <td></td> </tr> </tbody> </table> <p>NC – Not Count</p>	Defect	A	B	Bright Dot	1	NC	Dark Dot	2	Total	3			
Defect	A	B													
Bright Dot	1	NC													
Dark Dot	2														
Total	3														
d) 2 dot adjacent	<p>1 pair = 2 dots</p> <p>Type 1 Type 2</p> <p>Type 3 or Type 4</p>	<table border="1"> <thead> <tr> <th>Defect</th> <th>Acc. Count</th> </tr> </thead> <tbody> <tr> <td>2 Bright dot Adjacent</td> <td>0</td> </tr> <tr> <td>2 Dark dot Adjacent</td> <td>1</td> </tr> </tbody> </table>	Defect	Acc. Count	2 Bright dot Adjacent	0	2 Dark dot Adjacent	1							
Defect	Acc. Count														
2 Bright dot Adjacent	0														
2 Dark dot Adjacent	1														
2	<p>Black spot White Spot Bright spot Pin Hole Foreign Particle</p>	<p>-Black/Dark/Bright Spot is points on display which appear dark/bright and usually result from contamination - These defect do not vary in size intensity (contrast) when kontras is varied.</p> <p>$D = \frac{a+b}{2}(\text{mm})$</p>	<table border="1"> <thead> <tr> <th>Defect Category</th> <th>A</th> <th>B</th> </tr> </thead> <tbody> <tr> <td>$D \leq 0.10$</td> <td>NC</td> <td rowspan="4">NC</td> </tr> <tr> <td>$0.10 \leq D \leq 0.15$</td> <td>2</td> </tr> <tr> <td>$0.15 \leq D \leq 0.20$</td> <td>1</td> </tr> <tr> <td>$D \geq 0.2$</td> <td>0</td> </tr> </tbody> </table>	Defect Category	A	B	$D \leq 0.10$	NC	NC	$0.10 \leq D \leq 0.15$	2	$0.15 \leq D \leq 0.20$	1	$D \geq 0.2$	0
Defect Category	A	B													
$D \leq 0.10$	NC	NC													
$0.10 \leq D \leq 0.15$	2														
$0.15 \leq D \leq 0.20$	1														
$D \geq 0.2$	0														
3	<p>Black Line White line Particle between POL and Glass Scratch on Glass</p>		<table border="1"> <thead> <tr> <th>Defect Category</th> <th>A</th> <th>B</th> </tr> </thead> <tbody> <tr> <td>$W \leq 0.03$</td> <td>NC</td> <td rowspan="3">NC</td> </tr> <tr> <td>$0.03 \leq W \leq 0.05, L \leq 2.0$</td> <td>2</td> </tr> <tr> <td>$W \geq 0.05$</td> <td>0</td> </tr> </tbody> </table>	Defect Category	A	B	$W \leq 0.03$	NC	NC	$0.03 \leq W \leq 0.05, L \leq 2.0$	2	$W \geq 0.05$	0		
Defect Category	A	B													
$W \leq 0.03$	NC	NC													
$0.03 \leq W \leq 0.05, L \leq 2.0$	2														
$W \geq 0.05$	0														
4	<p>POL Bubble POL Dented</p>		<table border="1"> <thead> <tr> <th>Defect Category</th> <th>A</th> <th>B</th> </tr> </thead> <tbody> <tr> <td>$D \leq 0.20$</td> <td>NC</td> <td rowspan="4">NC</td> </tr> <tr> <td>$0.20 \leq D \leq 0.30$</td> <td>3</td> </tr> <tr> <td>$0.30 \leq D \leq 0.50$</td> <td>2</td> </tr> <tr> <td>$D \geq 0.5$</td> <td>0</td> </tr> </tbody> </table>	Defect Category	A	B	$D \leq 0.20$	NC	NC	$0.20 \leq D \leq 0.30$	3	$0.30 \leq D \leq 0.50$	2	$D \geq 0.5$	0
Defect Category	A	B													
$D \leq 0.20$	NC	NC													
$0.20 \leq D \leq 0.30$	3														
$0.30 \leq D \leq 0.50$	2														
$D \geq 0.5$	0														
5	<p>Mura (50% Grey)</p>		<p>Judged by Limit sample</p>												



11.0 Precaution and Limited Warranty

1. Handling Precautions

- a. The display panel is made of glass and polarizer. As glass is fragile. It tends to chip during handling especially on the edges. Please avoid dropping or jarring. Do not subject it to a mechanical shock of impact or by dropping it.
- b. If the display panel is damaged and the liquid crystal substance leaks out, be sure not to get any in your mouth. If the substance is in contact with your skin or clothes, wash it off using soap and water.
- c. Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary. Do not touch the display with bare hands. This will stain the display area and degrade the insulation between terminals. Scratch and dents may occur on polarizer too.
- d. The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully. Do not touch, push or rub the exposed polarizers with anything harder than a HB pencil lead (glass, tweezers, etc.). Do not put or attach anything on the display area to avoid leaving marks on it. Condensation on the surface and contact with terminals due to cold will damage, stain or dirty the polarizer. After products are tested at low temperature they must be warmed up in a container before coming in to contact with room temperature air.
- e. If the display surface becomes contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If it is heavily contaminated, moisten cloth with one of the following solvents
 - Isopropyl alcohol
 - Ethyl alcohol
 - Do not scrub hard to avoid damaging the display surface.
- f. Solvents other than those above-mentioned may damage the polarizer. Especially, do not use the following.
 - Water
 - Ketone
 - Aromatic solvents
 - Wipe off saliva or water drops immediately, contact with water over a long period of time may cause deformation or colour fading. Avoid contact with oil and fats.
- g. Exercise care to minimize corrosion of the electrode. Corrosion of the electrodes is accelerated by water droplets, moisture condensation or a current flow in a high-humidity environment.
- h. Install the LCD Module by using the mounting holes. When mounting the LCD module make sure it is free of twisting, warping and distortion. In particular, do not forcibly pull or bend the I/O cable or the backlight cable.
- i. Do not attempt to disassemble or process the LCD module.
- j. NC terminal should be open. Do not connect anything.
- k. If the logic circuit power is off, do not apply the input signals.
- l. Electro-Static Discharge Control. Since this module uses a CMOS LSI, the same careful attention should be paid to electrostatic discharge as for an ordinary CMOS IC. To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.
 - Before removing LCM from its packing case or incorporating it into a set, be sure the module and your body have the same electric potential. Be sure to ground the body when handling the LCD modules.
 - Tools required for assembly, such as soldering irons, must be properly grounded. Make certain the AC power source for the soldering iron does not leak. When using an electric screwdriver to attach LCM, the screw driver



should be of ground potentiality to minimize as much as possible any transmission of electromagnetic waves produced sparks coming from the commutator of the motor.

- To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions. To reduce the generation of static electricity be careful that the air in the work environment is not too dry. A relative humidity of 50%-60% is recommended. As far as possible make the electric potential of your work clothes and that of the work bench the ground potential.
 - The LCD module is coated with a film to protect the display surface. Exercise care when peeling off this protective film since static electricity may be generated.
- m. Since LCM has been assembled and adjusted with a high degree of precision, avoid applying excessive shocks to the module or making any alterations or modifications to it.
- Do not alter, modify or change the shape of the tab on the metal frame.
 - Do not make extra holes on the printed circuit board, modify its shape or change the positions of components to be attached.
 - Do not damage or modify the pattern writing on the printed circuit board.
 - Absolutely do not modify the zebra rubber strip (conductive rubber) or heat seal connector.
 - Except for soldering the interface, do not make any alterations or modifications with a soldering iron.
 - Do not drop, bend or twist the LCM.

2. Storage Precautions

When storing the LCD modules, the following precaution are necessary.

- a. Store them in a sealed polyethylene bag. If properly sealed, there is no need for the desiccant.
- b. Store them in a dark place. Do not expose to sunlight or fluorescent light, keep the temperature between 0°C and 35°C, and keep the relative humidity between 40%RH and 60%RH.
- c. The polarizer surface should not come in contact with any other objects.

3. Others

- a. Liquid crystals solidify under low temperature (below the storage temperature range) leading to defective orientation or the generation of air bubbles (black or white). Air bubbles may also be generated if the module is subject to a low temperature.
- b. If the LCD modules have been operating for a long time showing the same display patterns, the display patterns may remain on the screen as ghost images and a slight contrast irregularity may also appear. A normal operating status can be regained by suspending use for some time. It should be noted that this phenomenon does not adversely affect performance reliability.
- c. To minimize the performance degradation of the LCD modules resulting from destruction caused by static electricity etc. Exercise care to avoid holding the following sections when handling the modules.
 - Exposed area of the printed circuit board.
 - Terminal electrode sections.

4. Using LCD Modules

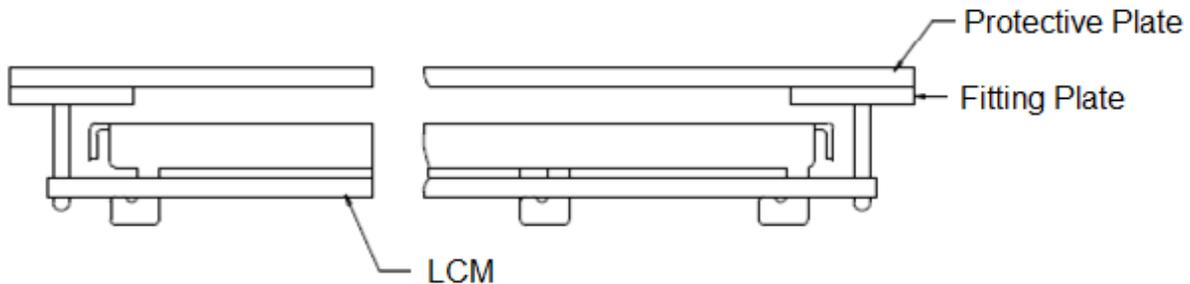
a. Installing LCD Modules

The hole in the printed circuit board is used to fix LCM as shown in the picture below.

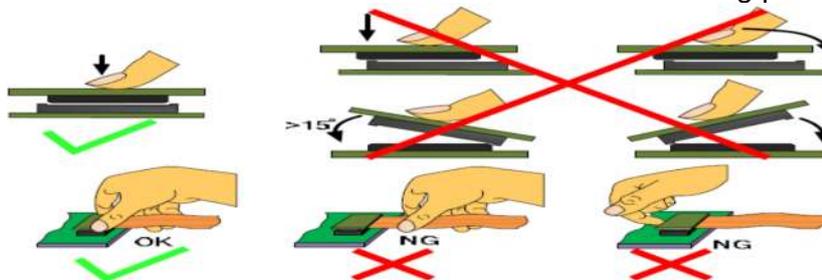


Attend to the following items when installing the LCM.

- b. Cover the surface with a transparent protective plate to protect the polarizer and LC cell.



- c. When assembling the LCM into other equipment, the spacer to the bit between the LCM and the fitting plate should have enough height to avoid causing stress to the module surface, refer to the individual specifications for measurements. The measurement tolerance should be 0.1mm.
- d. Precaution for assemble the module with BTB connector:
Please note the position of the male and female connector position, don't assemble or assemble like the method which the following picture shows



5. Precaution for soldering the LCM

	Manual soldering	Machine drag soldering	Machine press soldering
No RoHS Product	290°C ~350°C. Time: 3-5S.	330°C ~350°C. Speed: 4-8 mm/s.	300°C ~330°C. Time: 3-6S. Press: 0.8~1.2Mpa
RoHS Product	340°C ~370°C. Time: 3-5S.	350°C ~370°C. Time: 4-8 mm/s.	330°C ~360°C. Time: 3-6S. Press: 0.8~1.2Mpa

- a. If soldering flux is used, be sure to remove any remaining flux after finishing the soldering operation (This does not apply in the case of a non-halogen type of flux). It is recommended that you protect the LCD surface with a cover during soldering to prevent any damage due to flux spatters.
- b. When soldering the electroluminescent panel and PC board, the panel and board should not be detached more than three times. This maximum number is determined by the temperature and time conditions mentioned above, though there may be some variance depending on the temperature of the soldering iron.
- c. When removing the electroluminescent panel from the PC board, be sure the solder has completely melted, the soldered pad on the PC board could be damaged.



6. Precautions for Operation
 - a. Viewing angle varies with the change of liquid crystal driving voltage (VLCD). Adjust VLCD to show the best contrast.
 - b. It is recommended to drive LCD's within the specified voltage limit since over limit will cause shorter LCD life. An electrochemical reaction due to direct current causes LCD-deterioration. Avoid the use of direct current drive.
 - c. Response time will be extremely delayed at lower temperature compared to room operating temperature range and on the other hand, at higher temperature LCD shows dark color in them. However those phenomena do not mean malfunction. The LCD will return to normal performance when ambient temperature revert to room condition.
 - d. If the display area is pushed hard during operation, the display will become abnormal. However, it will return to normal if it is turned off and on.
 - e. A slight dew depositing on terminals is a cause for electro-chemical reaction resulting in terminal open circuit.
 - f. Input logic voltage before apply analogue high voltage such as LCD driving voltage when power on. Remove analogue high voltage before logic voltage when power off the module. Input each signal after the positive/negative voltage becomes stable.
 - g. Please keep the temperature within the specified range for use and storage. Polarization degradation, bubble generation or polarizer peel-off may occur with high temperature and high humidity.

7. Safety
 - a. It is recommended to crush damaged or unnecessary LCDs into pieces and wash them off with solvents such as acetone and ethanol, which should later be burned.
 - b. If any liquid leaks out of a damaged glass cell and comes in contact with the hands, wash off thoroughly with soap and water.

8. Limited Warranty

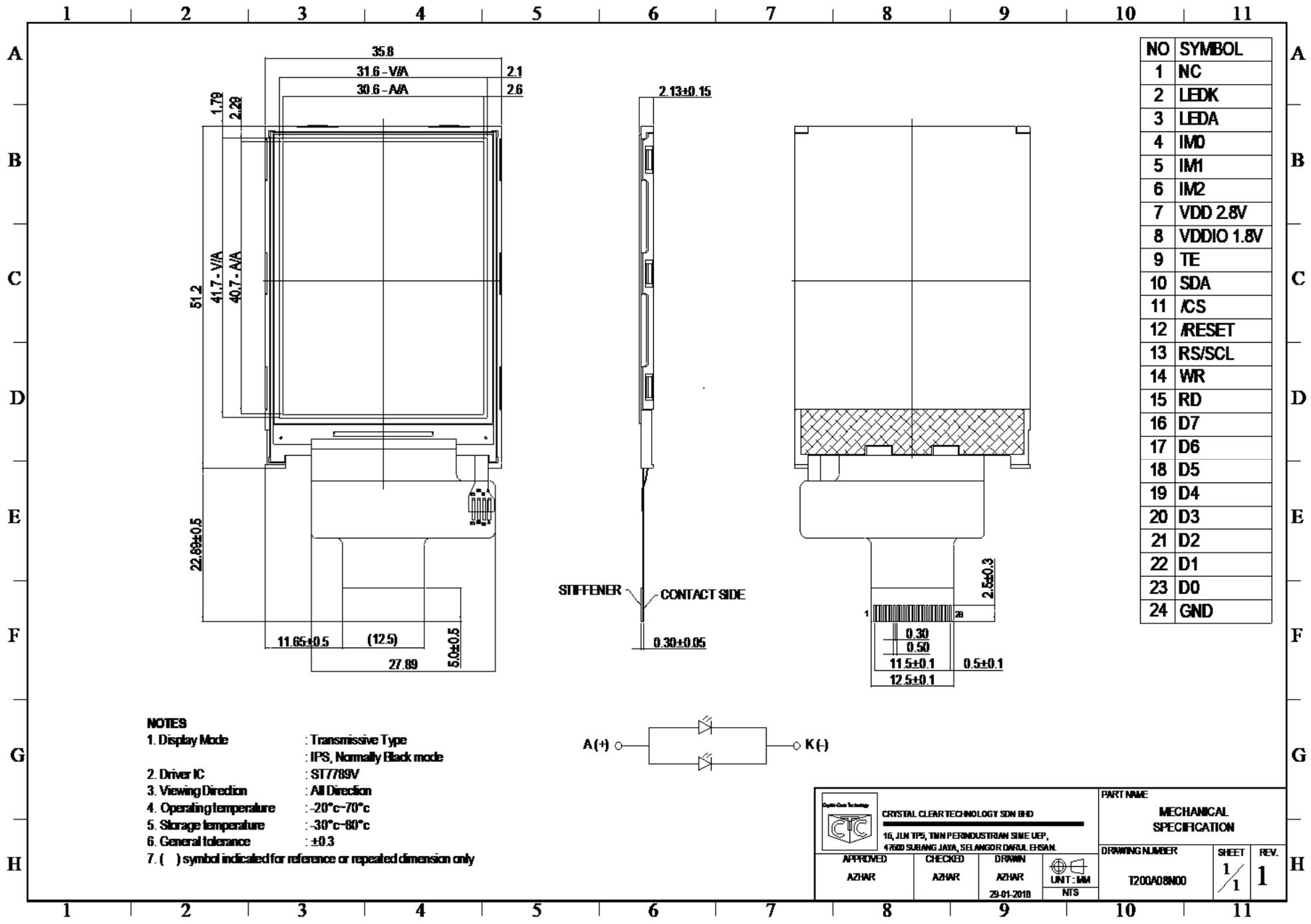
Unless otherwise agreed between Crystal Clear Technology and customer, Crystal Clear Technology will replace or repair any of its LCD and LCM which is found to be defective electrically and visually when inspected in accordance with Crystal Clear Technology acceptance standards, for a period of one year from date of shipment. Confirmation of such date shall be based on freight documents. The warranty liability of Crystal Clear Technology is limited to repair and/or replacement on the terms set forth above. Crystal Clear Technology will not responsible for any subsequent or consequential events.

9. Return LCM under Warranty

No warranty can be granted if the precautions stated above have been disregarded. The typical examples of violations are:

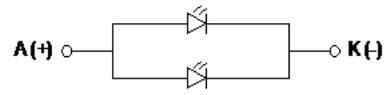
 - Broken LCD glass
 - PCB eyelet's damaged or modified
 - PCB conductors damaged
 - Circuit modified in any way, including addition of components.
 - PCB tampered with by grinding, engraving or painting varnish.
 - Soldering to, or modifying the bezel in any manner.

Module repairs will be invoiced to customer upon mutual agreement. Modules must be returned with sufficient description of failure or defects. Any connectors or cable installed by customer must be removed completely without damaging the PCB eyelet's, conductors and terminals.



NO	SYMBOL
1	NC
2	LEDK
3	LEDA
4	IMD
5	IM1
6	IM2
7	VDD 2.8V
8	VDDIO 1.8V
9	TE
10	SDA
11	CS
12	/RESET
13	RS/SCL
14	WR
15	RD
16	D7
17	D6
18	D5
19	D4
20	D3
21	D2
22	D1
23	D0
24	GND

- NOTES**
- 1. Display Mode : Transmissive Type
 - : IPS, Normally Black mode
 - 2. Driver IC : ST7789V
 - 3. Viewing Direction : All Direction
 - 4. Operating temperature : -20°C~70°C
 - 5. Storage temperature : -30°C~80°C
 - 6. General tolerance : ±0.3
 - 7. () symbol indicated for reference or repeated dimension only



	CRYSTAL CLEAR TECHNOLOGY SDN BHD 16, JLN TP5, TMM PERINDUSTRIAN SIME UEP, 47000 SUBANG JAYA, SELANGOR DARUL EHSAN.		PART NAME MECHANICAL SPECIFICATION	
	APPROVED AZHAR	CHECKED AZHAR	DRAWN AZHAR 29-01-2018	UNT : MM NTS
				SHEET 1 / 1
				REV. 1

