# Crystal Clear Technology

# **Product Specification**

G64128X25XXW00

(REPLACEMENT FOR G64128X09XXW00)

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# CRYSTAL CLEAR TECHNOLOGY SDN. BHD. Spec. No: G64128x25xxW00 REV 3.0

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#### 2.0 Record of revision

Rev	Date	Item	Page	Comment	Originator	Checked By
1.0	01/12/10			Initial Release	Thorn	Azhar
2.0	08/04/11	3	3	Update Module Numbering	Chong	Azhar
3.0	26/05/11	5	5	Update the current	Chong	Azhar
		9	10	Delete some un-used information		
		11	13	Update the power supply and		
				backlight (boosting circuit)		
4.0	20/06/13	5.1	4	Revise Backlight Lifetime	Azhar	Azhar



#### 3.0 General specification

Display format: Graphics 128 (w) x 64 (h) dots

Dot size: 0.48 (w) x 0.48 (h) mm

Dot pitch: 0.52 (w) x 0.52 (h) mm

View area: 70.7 (w) x 38.8 (h) mm

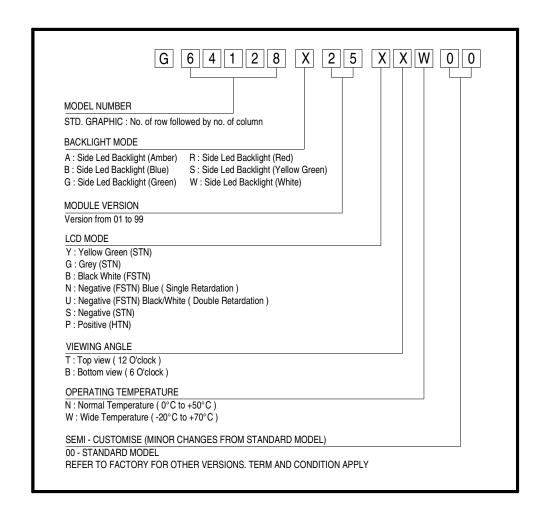
Active area: 66.52 (w) x 33.24 (h) mm

General dimensions: 76.10 (w) x 53.80 (h) x 6.0 (t) mm

Controller/Driver: ST7565P or equivalent

Interface: Parallel

Driving method: 1/64 duty, 1/9 bias





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#### 4.0 Absolute maximum rating (at Vss = 0V, Ambient temperature = $25^{\circ}C$ )

NO	ITEM	SIMBOL	MIN	MAX	UNIT
1.	Operating Voltage Range	$V_{ m DD}$	1.8	3.3	V
2.	Supply Voltage Range	$V_{LCD}$	-	10.2	V
3.	Input Voltage	V <sub>IN</sub>	-	-	V
4.	Operating Temperature	$T_{\mathrm{op}}$	Refer page 3		°C
5.	Storage Temperature	$T_{st}$	Refer page 3		°C

#### 5.0 Electrical characteristics

NO	ITEM	SYMBOL	CONDITION	MIN	TYP	MAX	UNIT
1.	Power Supply voltage (Logic)	$V_{DD} - V_{SS}$	-	2.7	3.0	3.3	V
2.	Power Supply voltage for LCD	V <sub>0</sub> - V <sub>SS</sub>	25°C	9.6±5%			V
3.	Current Supply	$I_{DD}$	$V_{DD} - V_{SS} = 3.0V$	-	0.2	0.4	mA

<sup>\*</sup>Note: Point 2, power supply voltage for LCD functions as a reference voltage. CCT is to set samples limit for contrast programming at factory level. This is to ensure consistent contrast ratio for all production LCD.

#### 5.1 Backlight Option

NO	Color	Forv	vard Vol (V)	ltage	Forward Current (mA)			Min Brightness (cd/m2)*
		Min	Тур	Max	Min	Тур	Max	
1.	White	-	4.0	-	-	60	80	60
2.	Blue	-	4.0	-	ı	60	80	30
3.	Green	6.2	6.6	7.0	-	60	-	150
4.	Yellow Green	-	4.0	-	-	60	120	40
5.	Amber	-	4.0	-	ı	60	120	60
6.	Red	-	4.0	-	-	60	120	60

\*Note: 1. Brightness Measured at backlight surface

2. On LCD surface, brightness is only about 10% to 15 % of backlight brightness.

3. Lifetime of backlight: For YG, Red, Amber = 50k hrs.

For White, Blue, Green = 20K hrs.

(Condition :  $Ta = 25^{\circ}C$  with typical current supply)





#### 6.0 Environmental and Reliability requirements

NO	ITEM	CONDITION
1.	Operating Temperature	Refer page 3
2.	Storage Temperature	Refer page 3
3.	Operating Humidity	Up to 90%RH
4.	Lifetime	50000 HOURS (excluding backlight)

Note: The background on LCD has the possibility to be changed in different temperature range.

TEST DESCRIPTION	TEST PARAMETERS	LCD	LCM
Temperature Cycle Operation	1 cycle = 0°C for 30 minutes, +70°C for 30minutes. Perform 10 cycles. LCM in operation.	N/A	Yes
Temperature Cycle Storage	1 cycle = -20°C for 30 minutes, +70°C for 30minutes. Perform 10 cycles. LCM in non-operation.	N/A	Yes
Boiling	LCD without polarizer. Boil LCD at 100°C @ 100% RH for 40 hours	Yes	N/A
Humidity Operation	+40°C at 90~95% RH for 250hours. *Applicable when fail boiling test	N/A	Yes

Note: Applicable to prototype and pre-production.



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7.0 LCD specification

7.1 Electro-optical characteristics (at ambient temperature =  $25^{\circ}$ C)

						]	LCD TYI	PE			
NO	ITEM	SYMBOL	CONDITION	STN YG	STN GREY	STN -VE BLUE/ PURP LE	FSTN +VE B/W	FSTN -VE BLUE	FSTN - VE TRUE B/W	FSTN -VE TRI AXIS	REF.
1	Operating Voltage (Volt)	$V_{LCD}$	$\theta = 0$ $Cr = max$				9.6 ± 5%	ó			7.1.1
		θ x 1	CD > 2	+25	+20	+35	+25	+35	+35	+40	
2	Viewing Angle	θ x 2	$CR \ge 2$ $V_{LCD} =$ $9.6V$	-25	-20	-35	-25	-35	-40	-40	7.1.2
	(Deg)	θу 1		-30	-25	-35	-30	-35	-35	-50	7.1.2
	(208)	θ y 2	7.0	+30	+25	+35	+30	+35	+35	+30	
3	Contrast Ratio	CR	$\theta = 0^{0}$ $V_{LCD}$ $= 9.6 V$	3.0	2.3	6.0	3.0	6.0	20	20	7.1.3
4	Response	Rise Time (Tr)	$\theta = 0^0$				200				7.1.4
4	Time (msec)	Decay Time (Td)	$\theta = 0^0$				250				7.1.4

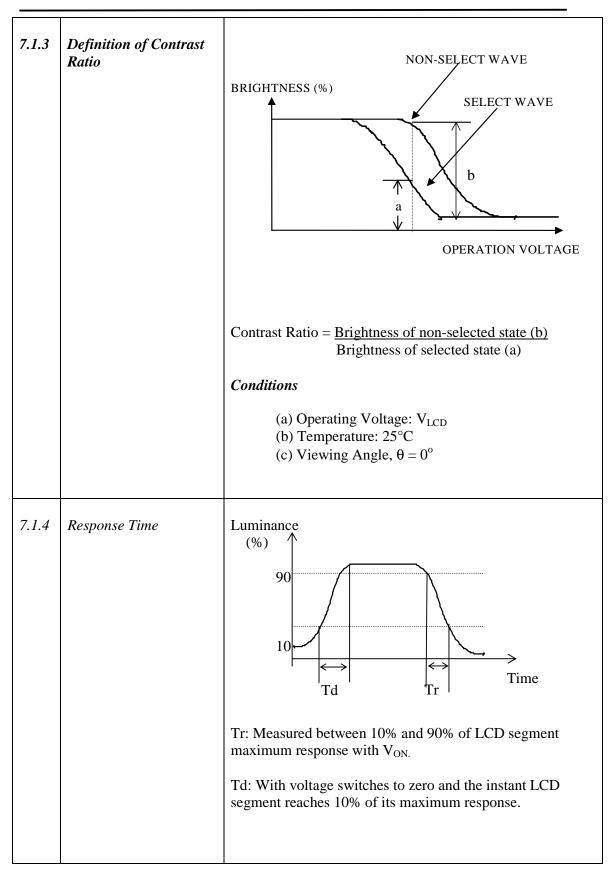
#### Note:

- 1. Viewing angle data is based on bottom view product by default. Should it be a top view product, values are then swap.
- 2. Contrast ratio is based on typical data when using white colour as backlight.
- 3. Equipment Used Eldim; Ez Contrast 120R, Spot Size = 2mm





NO	CHARACTERISTICS	<u>DEFINITIONS</u>
7.1.1	Definition of Operating Voltage ( $V_{LCD}$ )	$V_{LCD}$ $V_{LCD}$ : Operating Voltage F: Frame Frequency
7.1.2	Definition of Viewing Angle	TOP  θ REAR  FRONT  BOTTOM
		REAR ( $\theta$ y2)  LEFT( $\theta$ x2)  RIGHT( $\theta$ x1)  FRONT ( $\theta$ y1)

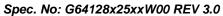




#### 8.0 Interface

8.1	Controller/Driver		ST7565P
8.2	Duty Cycle	1/64	
8.3	Pin out Assignments		
	Pin No	Symbol	Description
	1	Vss	Ground
	2	Vdd	Logic Power Supply
	3	Vout	Booster Output Voltage
	4	/Res	Read / Write execution control pin
	5	/CS1	Chip Select execution control pin
	6	A0	Data or Command execution control pin
	7	WR	Read / Write execution control pin
	8	Е	Enable execution control pin
	9	D0	8-bit MPU bus via the 8-bit bi-directional bus
	10	D1	8-bit MPU bus via the 8-bit bi-directional bus
	11	D2	8-bit MPU bus via the 8-bit bi-directional bus
	12	D3	8-bit MPU bus via the 8-bit bi-directional bus
	13	D4	8-bit MPU bus via the 8-bit bi-directional bus
	14	D5	8-bit MPU bus via the 8-bit bi-directional bus
	15	D6	8-bit MPU bus via the 8-bit bi-directional bus
	16	D7	8-bit MPU bus via the 8-bit bi-directional bus
	17	A	Backlight Anode Terminal
	18	K	Backlight Cathode Terminal







#### 9.0 Functional Descriptions

#### 9.1 Read/Write timing characteristics

## System Bus Read/Write Characteristics (For the 6800 Series MPU)

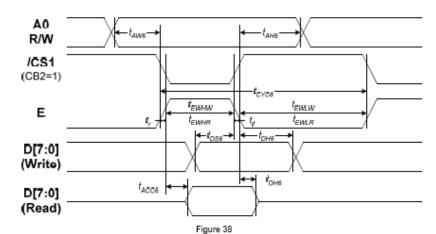


Table 27

			(	$V_{DD} = 3.3V_{+}$	Ta = -30  to	(35℃)
Item	Signal	Symbol	Condition	Rat	Units	
iteiii	Jigirai	Symbol	Condition	Min.	Max.	OIIICS
Address hold time		tAH6		0	_	
Address setup time	A0	†AW6		0	_	l
System cycle time		toyos		240	_	1
Enable L pulse width (WRITE)		tew.w		80	_	1
Enable H pulse width (WRITE)	E	tewnw		80	_	1
Enable L pulse width (READ)		tewlr		80	_	ns
Enable H pulse width (READ)		†EWHR		140		1
WRITE Data setup time		tD96		40	_	l
WRITE Address hold time	D0 to D7	tons		0	_	1
READ access time	DOTODI	tACC6	CL = 100 pF	_	70	1
READ Output disable time	1	tons	CL = 100 pF	5	50	l



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				(Voo = 2.7V,	Ta = -30 to	s5℃)
Item	Signal	Symbol	Condition		ing	Units
		<u> </u>		Min.	Max.	
Address hold time	_	†AH6		0	_	1
Address setup time	A0	tAW6		0	_	
System cycle time		tcycs		400	_	]
Enable L pulse width (WRITE)		<b>t</b> EWLW		220	_	]
Enable H pulse width (WRITE)	] [	tEWHW		180	_	]
Enable L pulse width (READ)	] -	†EWLR		220	_	ns
Enable H pulse width (READ)		†EWHR		180	_	]
WRITE Data setup time		tD96		40	_	1
WRITE Address hold time	D0 to D7	tons		0	_	1
READ access time	7 50 10 57	tACC6	CL = 100 pF	T -	140	1
READ Output disable time	7	tone.	CL = 100 pF	10	100	1

Table 29

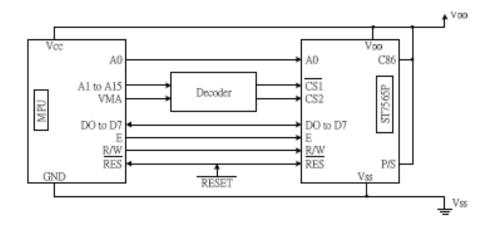
			(	$V_{DD} = 1.8V_{*}$	Ta = -30 to	85℃)
ltem	Signal	Symbol	Condition	Rat Min.	ing Max.	Units
Address hold time		†AH6		0	- max.	
Address setup time	A0	tAW6		0	_	1
System cycle time	1	toyos		640	_	1
Enable L pulse width (WRITE)		<b>t</b> EWLW		360	_	1
Enable H pulse width (WRITE)	] <sub>E</sub>	tEWHW.		280	_	1
Enable L pulse width (READ)	1 =	†EWLR		360	_	ns
Enable H pulse width (READ)	1	tewnr.		280	_	1
WRITE Data setup time		tD95		80	_	1
WRITE Address hold time	D0 to D7	tDH6		0	_	1
READ access time	D0 t0 D7	tACC6	CL = 100 pF	_	240	1
READ Output disable time	1	tons.	CL = 100 pF	10	200	1

<sup>&</sup>quot;1 The input signal rise time and fall time (tr, tr) is specified at 15 ns or less. When the system cycle time is extremely fast,  $(tr+tr) \le (tc y cs - tew Lw - tew Lw)$  for  $(tr+tr) \le (tc y cs - tew LR - tew LR)$  are specified.

#### Read/Write characteristics (6800 series MPU)

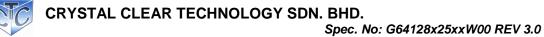
#### 9.2 **Application Circuits**

#### 6800 – Series Parallel Interface 9.2.1



<sup>&#</sup>x27;2 All timing is specified using 20% and 80% of Vpp as the reference.
'3 tewLw and tewLR are specified as the overlap between CS1 being "L" (CS2 = "H") and E.





#### 10. **Instruction Set**

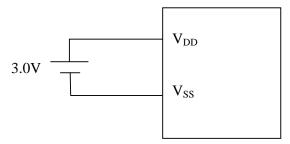
#### 10.1 Command Table

			DIC 10									protest and and and
Command	_					ind C		_				Function
	A0	/RD	MR					D3				
(1) Display ON/OFF	0	1	0	1	0	1	0	1	1	1	0 1	LCD display ON/OFF 0: OFF, 1: ON
(2) Display start line set	0	1	0	0	1	Di	spla	y sta	art a	ddre	255	Sets the display RAM display start line address
(3) Page address set	0	1	0	1	0	1	1	Pa	ge a	addr	ess	Sets the display RAM page address
<ul><li>(4) Column address set upper bit</li></ul>	0	1	0	0	0	0	1				cant	Sets the most significant 4 bits of the display RAM column address.
Column address set lower bit	0	1	0	0	0	0	0	Lea	st s	ignif	icant fress	Sets the least significant 4 bits of the display RAM column address.
(5) Status read	0	0	1		St	atus		0	0	0	0	Reads the status data
(6) Display data write	1	1	0			1	Vrit	e da	ta			Writes to the display RAM
(7) Display data read	1	0	1			I	Rea	d da	ta			Reads from the display RAM
(8) ADC select	0	1	0	1	0	1	0	0	0	0	0	Sets the display RAM address SEG output correspondence 0: normal, 1: reverse
(9) Display normal/ reverse	0	1	0	1	0	1	0	0	1	1	0	Sets the LCD display normal/ reverse 0: normal, 1: reverse
(10) Display all points ON/OFF	0	1	0	1	0	1	0	0	1	0	0	Display all points 0: normal display 1: all points ON
(11) LCD bias set	0	1	0	1	0	1	0	0	0	1	0	Sets the LCD drive voltage bias ratio 0: 1/9 bias, 1: 1/7 bias (ST7565P)
(12) Read/modify/write	0	1	0	1	1	1	0	0	0	0	0	Column address increment At write: +1 At read: 0
(13) End	0	1	0	1	1	1	0	1	1	1	0	Clear read/modify/write
(14) Reset	0	1	0	1	1	1	0	0	0	1	0	Internal reset
(15) Common output mode select	0	1	0	1	1	0	0	0 1	×	*	*	Select COM output scan direction 0: normal direction 1: reverse direction
(16) Power control set	0	1	0	0	0	1	0	1		pera ode	ting	Select internal power supply operating mode
(17) Vo voltage regulator internal resistor ratio set	0	1	0	0	0	1	0	0		esist atio	or	Select internal resistor ratio(Rb/Ra) mode
(18) Electronic volume mode set Electronic volume register set	0	1	0	1	0	0 Ele	0 ctro	0 nic v		0 me v	1 value	Set the Vo output voltage electronic volume register
(20) Booster ratio set	0	1	0	1	1	1	1	1	0		0 p-up slue	select booster ratio 00: 2x,3x,4x 01: 5x 11: 6x
(21) Power saver												Display OFF and display all points ON compound command
(22) NOP	0	1	0	1	1	1	0	0	0	1	1	Command for non-operation
(23) Test	0	1	0	1	1	1	1	×	*	*	×	Command for IC test. Do not use this command

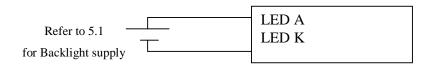


#### 11. Power Supply

11.1 Boosting circuit is provided in the LCD module.

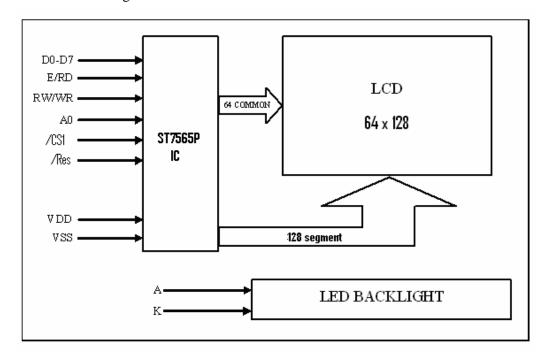


#### 11.2 Backlight power supply below:



For LED backlight version only

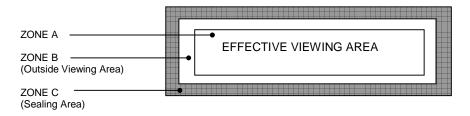
#### 12. Block Diagram





#### 13.0 Quality Assurance

#### 13.1 ZONE DEFINITION



## 13.2 REJECTION CRITERIA

#### 13.2.1 DIMENSIONAL DEFECTS

Defect Category	Defect Description	Criterion	Drawing Specification
Glass Size	Dimensions of LCD, do not conform to the drawing	Reject	Refer to LCD Physical Dimension Drawing
Perimeter Seal Extension	Perimeter seal epoxy enters the effective viewing area	Reject	
End Seal Size	Size of end seal does not meet drawing specification	Reject	Refer to LCD Physical Dimension Drawing

#### 13.2.2 VISUAL DEFECTS

Defect Category	Defect Description	Criterion	Drawing Specification
Fracture	A type of glass breakage containing running cracks. Inspectors should attempt to remove it with fingernail. If removed, evaluate as chip	Reject – if the size is ≥ 30% of the contact ledge width.	Fracture does not penetrat twough the whole glass thickness



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Defect Category	Defect Description	Criterion	Drawing Specification
Chip	Chip in cross over area	1) Reject - if the chip causes crossover dot to be exposed  2) Chip on outside edge of the glass plate but is greater than 50% of glass thickness at crossover dot is reject able.	Chip Epoxy of crossover dot exposed
Chip	Chip in contact pad area	Accept if:- a) X ≤ 2.0mm b) Y ≤ 0.5mm c) Z disregard	Z
	Chip in non- contact pad area	Accept if:- a) X ≤ 6.0mm b) Y ≤ 1.0mm c) Z disregard	X X X
	Chip in perimeter seal area	Accept if:- a) $Y \le 1/3$ of perimeter seal width (W) b) $X \le 3.0$ mm c) Z disregard d) X and Y not touch crossover dot	Z Z
Corner Chip	Corner chip within seal area	Accept if:- a) X ≤ 1/3 of perimeter seal width (W) b) Y ≤ 1/3 of perimeter seal width (W) c) Z disregard	Z W



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	Defect		
Defect Category	Description	Criterion	Drawing Specification
	Corner chip not effecting contact pad / ITO	Accept if:- a) $XY \le 4mm^2$ AND b) $Y \le D$ and $X \le 2.0mm$ c) Z disregard	y z
	Corner chip effecting contact pad / ITO	A) Accept if:- a) XY ≤ 4mm² AND b) Y ≤ D and X ≤ 2.0mm  B) Accept if:- a) X1 ≤ 2.0mm b) Y1 ≤ 0.5mm  Z disregard	A B
Glass flare	A thin layer of glass flare at contact area	Accept if:- a) Flare thickness \( \leq \) 1/4 W when W \( \leq \) 3mm b) Flare thickness \( \leq \) 1mm when W \( \leq \) 3mm W: Contact ledge width	
Glass burr  Rainbow	A rough edge(s) left along the scribing edge (i.e. along the edges of display)  Colored ring in sharp blotches observed	Reject – if the burr cause undersize or oversize of the LCD  Reject – if 3 or more colored rings in sharp blotches of color are observed. (Limit samples should be used when applicable)	Refer to LCD Physical Dimension Drawing



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Defect Category	Defect Description	Criterion	Drawing Specification
Discoloration		Reject - if the discolorations enter the active viewing area of LCD. Color of the LCD shall follow product specification as specified in the manufacturing specification	
Air Void	LC does not fulfill the display	Reject	
Fill end contamination	Discoloratio n at end seal area	Reject if discoloration exceeded the baffle (for display with baffle) or viewing area (for display without baffle)	

#### 13.2.3 POLARIZER DEFECT

Defect Category	Defect Description	Criterion	Drawing Specification
Polarizer defect	Polarizer coverage	<ol> <li>Polarizer should cover effective viewing area of display.</li> <li>It is acceptable if perimeter seal bolder at all sides could be seen.</li> <li>It is acceptable if polarizer attaching position meeting the tolerance mentioned in the drawing.</li> <li>It is reject able if polarizer edge jagged and not even</li> </ol>	Refer to LCD Physical Dimension Drawing
	Polarizer Peeling / delamination	1- Reject if any edge or corner of the polarizer is lifted up or not adheres to the glass	
	Polarizer Scratches	1- Any scratch should be acceptable if it is not visible from viewing distance at head of position 2-Polarizer scratch in viewing area is reject able if it is visible from the specified viewing distance 3-Defect, which is visible under surface glare, should be disregard	
	Polarizer damage	1-Stain mark or depression in front polarizer surface should be acceptable if it is not visible from viewing distance at head on position.  2-Defect, which is visible under surface glare, should be disregard	



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Defect Category	Defect Description	Crite	erion	Drawing Specification				
	Polarizer bubble /	Zone /				Acceptable No.		
	Foreign material	Dimension		A		100		
		D ≤ 0.15mm	NC	В	C	$\downarrow$ B		
		$0.15 < D \leq 0.30mm$	3	NC	NC	<b>←</b> A <b>→</b>		
		$0.30 < D \leq 0.50mm$	2	5	NC	D (A - D)/O		
		$0.50 < D \leq 1.0 mm$	0	3	NC	D = (A + B)/2		
		NC: No count		1	NC			
		D: Mean Diameter of Defe	ect					
		Accept - if air bubble not propagate into effe						

#### 13.2.4 FUNCTIONAL DEFECT

Defect Category	Defect Description	Criterion	Drawing Specification
Missing common	Part of the pattern does not light up	Reject	
Missing	One or few segment	Reject	
segment Common-	does not light up Common and	Delega	
common-	common and	Reject	
Segment-	Segment and	Reject	
segment short	segment connected	reject	
Common –	Common and	Reject	
segment short	segment connected		
Wrong	Wrong viewing	Reject if display viewing angle not conform	
viewing angle	angle	to customer requirement	
Metal residue	Extra spot lights up at the border of the segment.	Accept if ≤ 0.20mm (mean diameter)	
Slow response	Response of the display on one side slower than the other side	Reject if it is visible at 30cm distance	
Reverse twist/ tilt	Segment are darker or clearer than other area of the same segment	Reject	
Misalignment	Segment fatter or smaller or extra segment	Reject if > 10% of designed segment width and visible at 30cm distance	
Pin Hole	Pin hole / void at		↑ B
	light up segment	Zone / Acceptable No.	<b>」</b>
		Dimension A B C	<u> </u>
		D ≤ 0.10mm NC NC NC	D = (A + B)/2
		0.10 <d≤0.20mm 3="" no<="" td=""><td></td></d≤0.20mm>	
		NC: No count	
		D: Mean Diameter of Defect	



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Defect Category	Defect Description	Criterion	Drawing Specification
Segment	Light up segment	Reject	
Smearing	smear		
Dim segment	Display shows poor	Reject	
	contrast at pre set		
	voltage		

#### 13.2.5 BLACK SPOT, WHITE SPOT AND FOEREIGN MATERIAL

Defect Category	Defect Description	Crite	erion		Drawing Specification	
Black Spot,	Black Spot, White					
White Spot	Spot and Foreign	Zone /	Ace	ceptable N	No.	
and Foreign	Material	Dimension	A	В	С	↑ B
Material	Material	D <u>&lt;</u> 0.10mm	NC	NC	NC	
		0.10 <d 0.20mm<="" td="" ≤=""><td>3</td><td>3</td><td>NC</td><td>D = (A + B)/2</td></d>	3	3	NC	D = (A + B)/2
		$0.20 < D \le 0.30 mm$	1	2	NC	D = (A + B)/2
		D > 0.30 mm	0	0	NC	
		NC: No count				
		D: Mean Diameter of Defe	ect			

#### 13.2.6 LINE SHAPE AND SCRATCHES

Defect Category	Defect Description	Criterion				Drawing Specification	
Line shape and	Line shape and						
scratches	scratches	Zone /Dim	ension	Acceptable No.			
		X	Y	A	В	C	
		-	<0.01mm	NC	NC	NC	
		< 2 mm	< 0.02mm	1	1	NC	
		<1 mm	< 0.0 2mm	1	2	NC	

Note: Total defects shall not exceed five

Precaution for using LCM

#### 1. Liquid Crystal Display (LCD)

LCD is made up of glass, organic sealant, organic fluid and polymer based polarizers. The following precautions should be taken when handling.

- b) Keep the temperature within the range of use and storage. Excessive temperature and humidity could cause polarization degredation, polarizer peel off or bubble.
- Do not contact the exposed polarizer with anything harder than HB pencil lead. To clean dust off the display surface, wipe gently with cotton, chamois or other soft material soaked in petroleum benzin.
- Wipe off saliva or water drops immediately. Contact with water over a long period of time may cause polarizer deformation or colour fading, while an active LCD with water condensation on its surface will cause corrosion of ITO electrodes.
- Glass can be easily chipped or cracked from rough handling, especially at corners and edges.
- Do not drive LCD with DC voltage.

#### 2. Liquid Crystal Display Modules.

#### 2.1 Mechanical Considerations

LCM are assembled and adjusted with a high degree of precision. Avoid excessive shocks and do not make any alterations or modification. The following should be noted.

- a) Do not tamper in any way with the tabs on the metal frame.
- Do not modify the PCB by drilling extra holes, changing its outline, moving its component or modifying its pattern.
- Do not touch the elastomer connector, especially insert a backlight panel (for example, EL)
- When mounting a LCM make sure that the PCB is not under any stress such as bending or twisting. Elastomer contacts are very delicate and missing pixels could result from slight dislocation of any of the elements.

Avoid pressing on the metal bezel, otherwise the elastomer connector could be deformed and lose contact, resulting in missing pixels.

Spec. No: G64128x25xxW00 REV 3.0

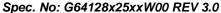
#### 2.2 Static Electricity

LCM contains CMOS LSI's and the same precaution for such devices should apply, namely

- The operator should be grounded whenever he/she comes into contact with the module. Never touch any of the conductive parts such as the LSI pads, the copper leads on the PCB and the interface terminals with any parts of the human body.
- b) The modules should be kept in antistatic bags or other containers to static for storage.
- Only properly grounded soldering irons should be used.
- If an electric screwdriver is used, it should be well grounded and shielded from commutator spark.
- The normal static prevention measures should be observed for work clothes and working benches, the latter conductive (rubber) mat is recommended.
- Since dry air is inductive to statics, a relative humidity of 50-60% is recommended.

#### 2.3 Soldering

- Solder only to the I/O terminals.
- b) Use only soldering irons with proper grounding and no leakage.
- Soldering temperature: 280 °C c)
- Soldering time: 3 to 4 sec d)
- Use eutectic solder with resin flux fill.
- If flux is used, the LCD surface should be covered to avoid flux spatters. Flux residue should be removed afterwards.





#### 2.4 Operation

- The contras can be adjusted by varying the LCD driving voltage V0
- b) Driving voltage should be kept within specified range, excess voltage shortens display life.
- Response time increases with decrease in temperature.
- d) Display may turn black or dark blue at temperature above its operational range, this is (however not pressing on the viewing area) may cause the segments to appear "fractured".
- e) Mechanical disturbance during operation ( such as pressing on the viewing area) may cause the segments to appear "fractured".

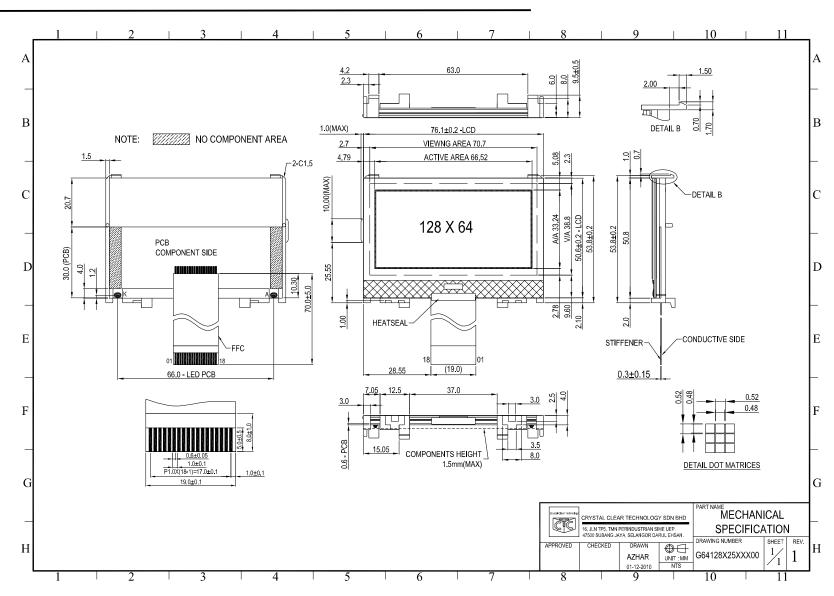
#### 2.5 Storage

If any fluid leaks out of the damage glass cell, wash off any human part that comes into contact with soap and water. Never swallow the fluid. The toxicity is extremely low but caution should be exercised at all the time.

#### 2.6 Limited Warranty

Unless otherwise agreed between Crystal Clear Technology and customer, Crystal Clear Technology will replace or repair any of its LCD and LCM which is found to be defective electrically and visually when inspected in accordance with Crystal Clear Technology acceptance standards, for a period of one year from date of shipment. Confirmation of such date shall be based on freight documents. The warranty liability of Crystal Clear Technology is limited to repair and/or replacement on the terms set forth above. Crystal Clear Technology will not responsible for any subsequent or consequential events.







## **Crystal Clear Technology**

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