

# **Product Specification**

# G1225x02 series

Crystal Clear Technology sdn. bhd.

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## 2.0 Record of revision

| Rev | Date     | Item  | Page | Comment                           | Originator | Checked By |
|-----|----------|-------|------|-----------------------------------|------------|------------|
| 1.0 | 30/09/08 |       |      | Initial Release                   | Syam       | Azhar      |
| 2.0 | 31/10/08 | 5.0   | 4    | Vdd values update 3.0V typ and    | Syam       | Azhar      |
|     |          |       |      | 3.3V max.                         |            |            |
|     |          | 9.2.4 | 13   | Figure error. Corrected from IF1, |            |            |
|     |          |       |      | IF2, IF3 tie to high (Vdd),       |            |            |
|     |          |       |      | changed to all tie to low (Vss).  |            |            |
|     |          |       |      |                                   |            |            |
|     |          |       |      |                                   |            |            |
|     |          |       |      |                                   |            |            |
|     |          |       |      |                                   |            |            |
|     |          |       |      |                                   |            |            |
|     |          |       |      |                                   |            |            |
|     |          |       |      |                                   |            |            |
|     |          |       |      |                                   |            |            |
|     |          |       |      |                                   |            |            |
|     |          |       |      |                                   |            |            |
|     |          |       |      |                                   |            |            |
|     |          |       |      |                                   |            |            |
|     |          |       |      |                                   |            |            |
|     |          |       |      |                                   |            |            |
|     |          |       |      |                                   |            |            |
|     |          |       |      |                                   |            |            |
|     |          |       |      |                                   |            |            |
|     |          |       |      |                                   |            |            |
|     |          |       |      |                                   |            |            |
|     |          |       |      |                                   |            |            |
|     |          |       |      |                                   |            |            |
|     |          |       |      |                                   |            |            |
|     |          |       | L    |                                   |            |            |



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## 3.0 General specification

Display format: Graphics 255 (w) x 128 (h) dots

Dot size: 0.28 (w) x 0.28 (h) mm

Dot pitch: 0.30 (w) x 0.30 (h) mm

View area: 82.0 (w) x 43.0 (h) mm

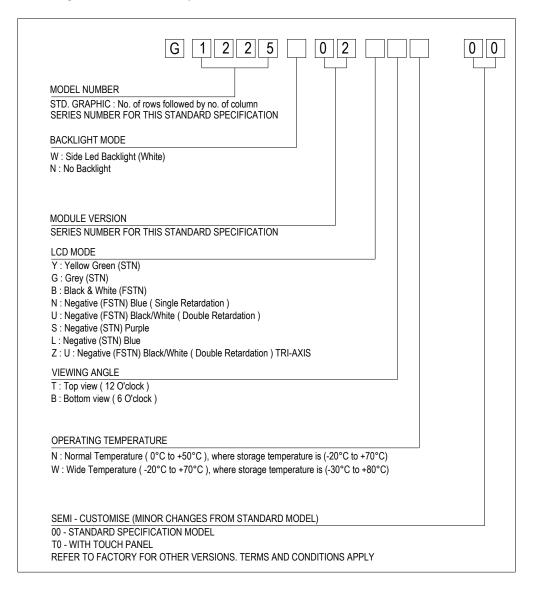
Active area: 76.48 (w) x 38.38 (h) mm

General dimensions: 110.7 (w) x 63.2 (h) x 6.1 (t) mm

Controller/Driver: ST7529 or equivalent

Interface: Parallel/Serial

Driving method: 1/144 duty, 1/11 bias





| NO | ITEM                    | SIMBOL           | MIN          | MAX          | UNIT |
|----|-------------------------|------------------|--------------|--------------|------|
| 1. | Operating Voltage Range | V <sub>DD</sub>  | -0.5         | 5.0          | V    |
|    |                         | V <sub>DDA</sub> | -0.5         | 5.0          |      |
| 2. | Operating Temperature   | T <sub>op</sub>  | Refer p      | Refer page 3 |      |
| 3. | Storage Temperature     | T <sub>st</sub>  | Refer page 3 |              | °C   |

## 4.0 Absolute maximum rating (at Vss = 0V, ambient temperature = $25^{\circ}$ C)

## 5.0 Electrical characteristics

| NO | ITEM                                     | SYMBOL                              | CONDITION        | MIN | TYP    | MAX | UNIT |
|----|--|-------------------------------------|------------------|-----|--------|-----|------|
| 1. | Operating Voltage                        | V <sub>DD</sub><br>V <sub>DDA</sub> | -                | -   | 3.0    | 3.3 | V    |
| 2. | Power Supply voltage (V <sub>LCD</sub> ) |                                     | 25°C             | 1   | 4.5±5% | )   | V    |
| 3. | Current Supply                           | I <sub>DD</sub>                     | $V_{DD} = 3.3 V$ | -   | 3.2    | -   | mA   |

#### 5.1 Backlight Options

| NO | COLOR | FORW | ARD VO<br>(V) | LTAGE | LTAGE FORWARD CURRENT<br>(mA) |      |     | MIN<br>BRIGHTNESS |  |
|----|-------|------|---------------|-------|-------------------------------|------|-----|-------------------|--|
|    |       | Min  | Тур.          | Max   | Min                           | Тур. | Max | (cd/m2) *         |  |
| 1. | White | -    | 4.0           | -     | -                             | 75   | 100 | 250               |  |
|    |       |      |               |       |                               |      |     |                   |  |

\*Note : 1. Brightness measured at backlight surface.

2. On LCD surface, brightness is only about 10% to 15% of backlight brightness.

3. Lifetime of backlight: For YG, Amber, Red = 50K hrs. For White, Blue = 20K hrs

## 6.0 Environmental requirements

| NO | ITEM                | CONDITION                                |
|----|---------------------|--|
| 1. | Operating           | Refer page 3                             |
|    | Temperature         |  |
| 2. | Storage Temperature | Refer page 3                             |
| 3. | Operating Humidity  | 5% to 95%RH                              |
| 4. | Cycle Test          | 0 C @ 30 min to 50 C @ 30min for 1 cycle |
|    |                     | run for 10 cycles                        |
| 5. | Lifetime            | 50000 HOURS (excluding backlight)        |

Note: The background on LCD has the possibility to be changed in different temperature range.



## 7.0 LCD specification

|    |                                |                       |  |           |             | l                                 | LCD TYI            | PE                  |                             |                            |       |
|----|--------------------------------|-----------------------|--|-----------|-------------|-----------------------------------|--------------------|---------------------|-----------------------------|----------------------------|-------|
| NO | ITEM                           | SYMBOL                | CONDITION  | STN<br>YG | STN<br>GREY | STN<br>-VE<br>BLUE/<br>PURP<br>LE | FSTN<br>+VE<br>B/W | FSTN<br>-VE<br>BLUE | FSTN -<br>VE<br>TRUE<br>B/W | FSTN<br>-VE<br>TRI<br>AXIS | REF.  |
| 1  | Operating<br>Voltage<br>(Volt) | V <sub>LCD</sub>      | $\theta = 0$<br>Cr = max   |           |             |                                   | $14.5 \pm 59$      | %                   |                             |                            | 7.1.1 |
|    | × <i>x</i> · ·                 | θx 1                  |  | +20       | +15         | +35                               | +20                | +35                 | +30                         | +40                        |       |
| 2  | Viewing                        | θx 2                  | $CR \ge 2$   | -20       | -15         | -35                               | -20                | -35                 | -35                         | -40                        | 7.1.2 |
| 2  | Angle<br>(Deg)                 | θy 1                  | $V_{LCD} =$<br>14.7V   | -25       | -20         | -30                               | -25                | -30                 | -30                         | -50                        | 1.1.2 |
|    | (1968)                         | θ y 2                 | 17.7 4   | +25       | +20         | +30                               | +25                | +30                 | +30                         | +30                        |       |
| 3  | Contrast<br>Ratio              | CR                    | $\begin{array}{c} \theta = 0^{0} \\ V_{LCD} \\ = 14.7 V \end{array}$ | 2.5       | 2.0         | 5.5                               | 2.5                | 5.5                 | 15                          | 15                         | 7.1.3 |
|    | Response                       | Rise<br>Time<br>(Tr)  | $\theta = 0^0$   |           | 400         |                                   |                    |                     |                             |                            | 714   |
| 4  | 4 Time<br>(msec)               | Decay<br>Time<br>(Td) | $\theta = 0_0$   |           |             |                                   | 400                |                     |                             |                            | 7.1.4 |

## 7.1 Electro-optical characteristics (at ambient temperature = $25^{\circ}$ C)

Note:

- 1. Viewing angle data is based on bottom view product by default. Should it be a top view product, values are then swap.
- 2. Contrast ratio is based on typical data when using white colour as backlight.
- 3. Equipment Used Eldim; Ez Contrast 120R, Spot Size = 2mm

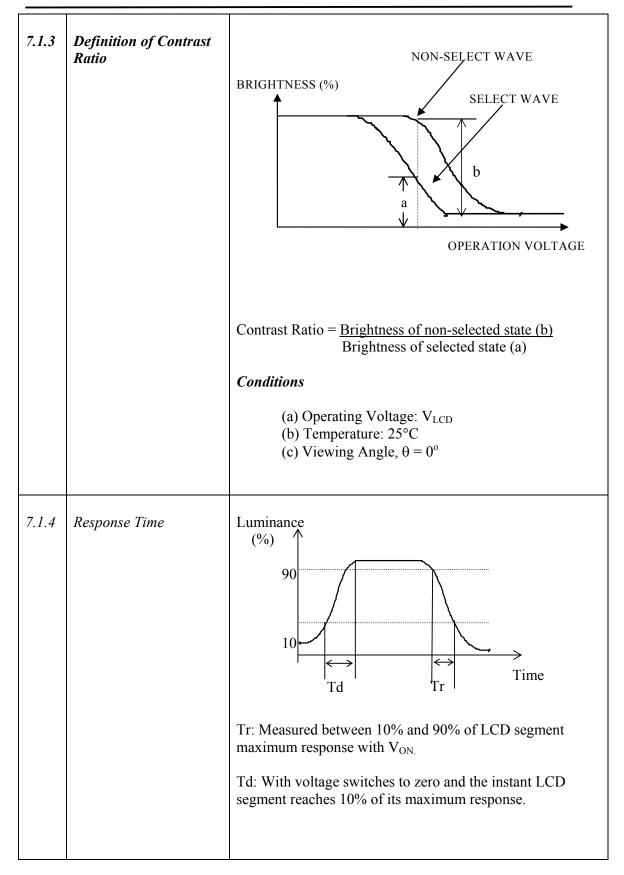


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| NO    | <b>CHARACTERISTICS</b>                                 | DEFINITIONS  |
|-------|--|--|
| 7.1.1 | Definition of Operating<br>Voltage (V <sub>LCD</sub> ) | $V_{LCD}$<br>$V_{LCD}$<br>$V_{LCD}$ : Operating Voltage<br>F : Frame Frequency           |
| 7.1.2 | Definition of Viewing<br>Angle                         | TOP<br>θ REAR<br>LEFT  |
|       |  | REAR ( $\theta$ y2)<br>LEFT( $\theta$ x2)<br>RIGHT( $\theta$ x1)<br>FRONT ( $\theta$ y1) |



Spec. No: G1225x02xxx00 REV 2.0

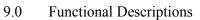




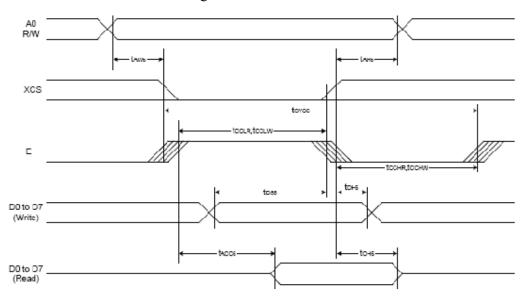
## 8.0 Interface

| 8.1 | Display Driver | ST7529 OF | REQUIVALENT  |
|-----|----------------|-----------|--|
| 8.2 | Pin No         | Symbol    | Description  |
|     | 1              | V0        | LCD driving voltage  |
|     | 2              | V1        | LCD driving voltage  |
|     | 3              | V2        | LCD driving voltage  |
|     | 4              | V3        | LCD driving voltage  |
|     | 5              | V4        | LCD driving voltage  |
|     | 6              | VLCD      | LCD driving voltage  |
|     | 7              | C6P       | Positive connection for capacitor 6  |
| -   | 8              | C4P       | Positive connection for capacitor 4  |
| -   | 9              | C2N       | Negative connection for capacitor 2  |
|     | 10             | C2P       | Positive connection for capacitor 2  |
|     | 11             | C1P       | Positive connection for capacitor 1  |
|     | 12             | C1N       | Negative connection for capacitor 1  |
|     | 13             | C3P       | Positive connection for capacitor 3  |
|     | 14             | C5P       | Positive connection for capacitor 5  |
|     | 15             | VDDA      | Internal generator supply  |
|     | 16             | VSS       | Ground   |
|     | 17             | VSS       | Ground   |
|     | 18             | VDD       | Logic supply voltage   |
|     | 19             | XCS       | Chip select input pin  |
|     | 20             | SCL       | Serial clock input pin   |
|     | 21             | SI        | Serial data input pin  |
|     | 22             | IF3       | Interface mode selection   |
|     | 23             | IF2       | Interface mode selection   |
|     | 24             | IF1       | Interface mode selection   |
|     | 25             | RST       | Reset input active low   |
|     | 26             | ERD       | R/W : 68000 Series Parallel Interface Read & WriteControl InputRD : 8080 Series Parallel Interface Read EnableClock Input          |
|     | 27             | D7        | 8 bit bi-directional data bus  |
|     | 28             | D6        | 8 bit bi-directional data bus  |
|     | 29             | D5        | 8 bit bi-directional data bus  |
|     | 30             | D4        | 8 bit bi-directional data bus  |
|     | 31             | D3        | 8 bit bi-directional data bus  |
|     | 32             | D2        | 8 bit bi-directional data bus  |
|     | 33             | D1        | 8 bit bi-directional data bus  |
|     | 34             | D0        | 8 bit bi-directional data bus  |
|     | 35             | EWR       | R/W : 68000 Series Parallel Interface Read & Write<br>Control InputRD : 8080 Series Parallel Interface Write Enable<br>Clock Input |
|     | 36             | A0        | Register select input pin  |





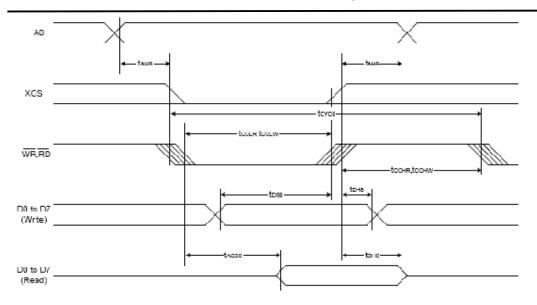
9.1 Read/Write timing characteristics



| Item                         | Ginnal   | Sumbol | Condition   | Rati | Units |       |
|------------------------------|----------|--------|-------------|------|-------|-------|
| nem                          | Signal   | Symbol | Condition   | Min. | Max.  | Units |
| Address hold time            |          | tAH6   | -           | 20   | -     |       |
| Address setup time           | AO       | tAW6   | -           | 20   | -     | ]     |
| System cycle time            |          | tCYC6  | -           | 400  | -     | ]     |
| Enable L pulse width (WRITE) | WR       | tEWLW  | -           | 200  | -     |       |
| Enable H pulse width (WRITE) | WIX      | tEWHW  | -           | 200  | -     | ]     |
| Enable L pulse width (READ)  | RD       | tEWLR  | -           | 200  | -     | ns    |
| Enable H pulse width (READ)  | , KD     | tEWHR  | -           | 200  | -     | ]     |
| WRITE Data setup time        |          | tDS6   | -           | 200  | -     | ]     |
| WRITE Address hold time      | D0 to D7 | tDH6   | -           | 20   | -     |       |
| READ access time             | 001007   | tACC6  | CL = 100 pF | -    | 40    | ]     |
| READ Output disable time     |          | tOH6   | CL = 100 pF | -    | 30    |       |

Read/Write characteristics (6800 series MPU)

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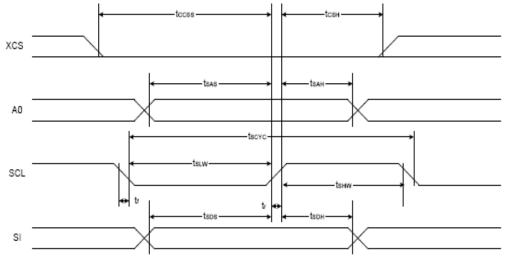


| Gignal   | Sumbol                               | Condition   | Ratir   | ng   | Units   |
|----------|--------------------------------------|---|---|--|---|
| Signai   | Symbol                               | Symbol Condition  |   | Max.   |   |
|          | tAH8                                 | -   | 20  | -  |   |
| AO       | tAW8                                 | -   | 20  | -  | ]   |
|          | tCYC8                                | -   | 400   | -  | 1   |
| WB       | tCCLW                                | -   | 200   | -  | 1   |
| WK       | tCCHW                                | -   | 200   | -  | ]   |
|          | tCCLR                                | -   | 200   | -  | ns  |
| RD       | tCCHR                                | -   | 200   | -  | ]   |
|          | tDS8                                 | -   | 200   | -  | ]   |
| D0 to D7 | tDH8                                 | -   | 20  | -  | ]   |
| 001007   | tACC8                                | CL = 100 pF   | -   | 40   | ]   |
|          | tOH8                                 | CL = 100 pF   | -   | 30   |   |
|          | Signal<br>A0<br>WR<br>RD<br>D0 to D7 | Image: bold state s | Image: constraint of the second sec | Signal         Symbol         Condition           Min.         Min.           A0         tAH8         -         20           tAW8         -         20           tCYC8         -         400           tCYC8         -         400           WR         tCCLW         -         200           tCCHW         -         200           RD         tCCLR         200           tCCHR         -         200           tCCHR         -         200           tD0 to D7         tD88         -         200           tDCR         CL = 100 pF         - | Min.         Max.           A0         tAH8         -         20         -           A0         tAW8         -         20         -           tCYC8         -         400         -           WR         tCCLW         -         200         -           WR         tCCLW         -         200         -           RD         tCCLR         -         200         -           RD         tCCHR         -         200         -           tD0 to D7         tDH8         -         200         -           tACC8         CL = 100 pF         -         40 |

Read/Write characteristics (8080 series MPU)



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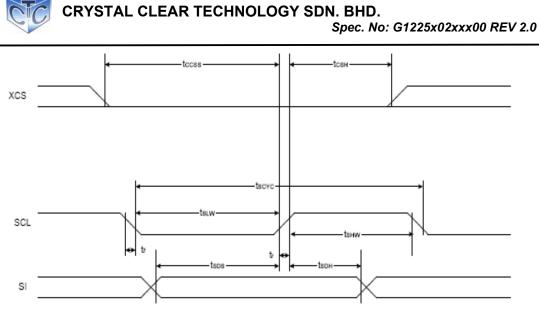




(V<sub>DD</sub>=3.3V,Ta= -30 to 85°C,Die )

| Item                | Cinnal | C. maked | Condition | Rati | Units |       |
|---------------------|--------|----------|-----------|------|-------|-------|
| Item                | Signal | Symbol   | Condition | Min. | Max.  | Units |
| Serial Clock Period |        | tSCYC    | -         | 100  | -     |       |
| SCL "H" pulse width | SCL    | tSHW     | -         | 50   | -     | ]     |
| SCL "L" pulse width |        | tSLW     | -         | 50   | -     | ]     |
| Address setup time  |        | tSAS     | -         | 40   | -     | ]     |
| Address hold time   | AD     | tSAH     | -         | 30   | -     | ns    |
| Data setup time     | si     | tSDS     | -         | 30   | -     | 1     |
| Data hold time      | 1 51   | tSDH     | -         | 30   | -     | ]     |
| CS-SCL time         | xcs    | tCSS     | -         | 20   | -     | ]     |
| CS-SCL time         | 1 105  | tCSH     | -         | 50   | -     | 1     |

Read/Write characteristics (Serial Interface 4 Line)





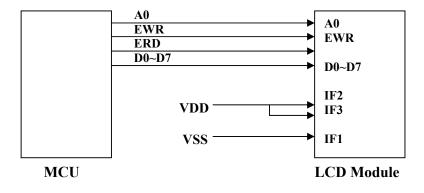
(V<sub>DD</sub>=3.3V,Ta= -30 to 85°C,Die)

| li                  | Cinnal | Cumb al | Condition | Rati | ing  | Units |
|---------------------|--------|---------|-----------|------|------|-------|
| Item                | Signal | Symbol  | Condition | Min. | Max. | Units |
| Serial Clock Period |        | tSCYC   | -         | 100  | -    |       |
| SCL "H" pulse width | SCL    | tSHW    | -         | 50   | -    |       |
| SCL "L" pulse width |        | tSLW    | -         | 50   | -    | ]     |
| Data setup time     | ~      | tSDS    | -         | 30   | -    | ns    |
| Data hold time      | SI     | tSDH    | -         | 30   | -    |       |
| CS-SCL time         | xcs    | tCSS    | -         | 20   | -    | ]     |
| CS-SCL time         | 705    | tCSH    | -         | 50   | -    |       |

Read/Write characteristics (Serial Interface 3 Line)

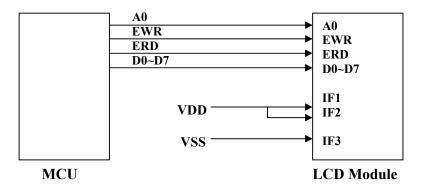
## 9.2 Application Circuits

9.2.1 6800 – Series Parallel Interface

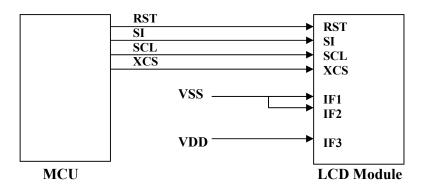




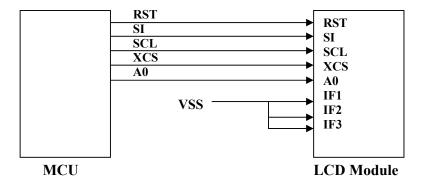
9.2.2 8080 – Series Parallel Interface



9.2.3 3 Lines Serial Interface



9.2.4 4 Lines Serial Interface





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## 10. Instruction set

Ext=0 or Ext=1

| Notice of the | Index | Command | A0 | RD | WR | D7 | DG | D5 | D4 | DЭ | D2 | D1 | DO | Function  | //ex | Parameter |
|---------------|-------|---------|----|----|----|----|----|----|----|----|----|----|----|-----------|------|-----------|
| 1000          | 1     | ExtIn   | 0  | 1  | 0  | 0  | 0  | 1  | 1  | 0  | 0  | 0  | 0  | Ext-0 Set | 30   | None      |
| 10000         | 2     | ExtOut  | 0  | 1  | 0  | 0  | 0  | 1  | 1  | 0  | 0  | 0  | 1  | Ext=1 Set | 31   | None      |

## Ext=0

| Index      | Command  | A0 | RD | WR | D7 | D6 | D5 | D4 | D3 | D2 | D1 | DO | Function              | Нох | Parameter |
|------------|----------|----|----|----|----|----|----|----|----|----|----|----|-----------------------|-----|-----------|
| 1          | DISON    | n  | 1  | Ο  | 1  | n  | 1  | 0  | 1  | 1  | 1  | 1  | Display On            | AF  | None      |
| 2          | DISOFF   | 0  | 1  | 0  | 1  | 0  | 1  | 0  | 1  | 1  | 1  | 0  | Display Off           | AE  | None      |
| 3          | DISNOR   | 0  | 1  | 0  | 1  | 0  | 1  | 0  | 0  | 1  | 1  | 0  | Normal Display        | A6  | None      |
| 4          | DISINV   | U  | 1  | υ  | 1  | U  | 1  | υ  | U  | 1  | 1  | 1  | Inverse Display       | A/  | None      |
| 5          | COMSON   | 0  | 1  | 0  | 1  | 0  | 1  | 1  | 1  | 0  | 1  | 1  | COM Scan Direction    | BE  | 1 byte    |
| 6          | DISCTRL  | 0  | 1  | 0  | 1  | 1  | 0  | 0  | 1  | 0  | 1  | 0  | Display Control       | CA  | 3 bytes   |
| 7          | SLPIN    | 0  | 1  | 0  | 1  | 0  | 0  | 1  | 0  | 1  | 0  | 1  | Sleep in              | 95  | None      |
| 8          | SI POUT  | n  | 1  | n  | 1  | n  | n  | 1  | 0  | 1  | n  | n  | Sleep Out             | 94  | None      |
| 9          | LASET    | 0  | 1  | 0  | 0  | 1  | 1  | 1  | 0  | 1  | 0  | 1  | Line Address Set      | 75  | 2 bytes   |
| 10         | CASET    | 0  | 1  | 0  | 0  | 0  | 0  | 1  | 0  | 1  | 0  | 1  | Column Address Set    | 15  | 2 bytes   |
| 11         | DAISDR   | υ  | 1  | υ  | 1  | U  | 1  | 1  | 1  | 1  | υ  | υ  | Data Scan Direction   | вс  | 3 bytes   |
| 12         | RAMWR    | 0  | 1  | 0  | 0  | 1  | 0  | 1  | 1  | 1  | 0  | 0  | Writing to Memory     | 5C  | Dala      |
| 13         | RAMRD    | 0  | 1  | 0  | 0  | 1  | 0  | 1  | 1  | 1  | 0  | 1  | Reading from Memory   | 5D  | Data      |
| 14         | PTLIN    | 0  | 1  | 0  | 1  | 0  | 1  | 0  | 1  | 0  | 0  | 0  | Partial display in    | ٨8  | 2 bytes   |
| 15         | PTI OUT  | n  | 1  | n  | 1  | n  | 1  | 0  | 1  | n  | 0  | 1  | Partial display out   | Α9  | None      |
| 16         | RMWIN    | 0  | 1  | 0  | 1  | 1  | 1  | 0  | 0  | 0  | 0  | 0  | Read and Modify Write | EO  | None      |
| 17         | RMWOUT   | 0  | 1  | 0  | 1  | 1  | 1  | 0  | 1  | 1  | 1  | 0  | RMW end               | EE  | None      |
| 18         | ASCSET   | υ  | 1  | υ  | 1  | U  | 1  | υ  | 1  | υ  | 1  | υ  | Area Scroll Set       | AA  | 4 bytes   |
| 19         | SCSTART  | 0  | 1  | 0  | 1  | 0  | 1  | 0  | 1  | 0  | 1  | 1  | Scrull Start Set      | AB  | 1 byle    |
| 20         | OSCON    | 0  | 1  | 0  | 1  | 1  | 0  | 1  | 0  | 0  | 0  | 1  | Internal CSC on       | D1  | None      |
| 21         | OSCOFF   | 0  | 1  | 0  | 1  | 1  | 0  | 1  | 0  | 0  | 1  | 0  | Internal CSC off      | D2  | Ncne      |
| <i>7</i> 2 | PWRCTRI  | Ο  | 1  | Ο  | n  | n  | 1  | 0  | 0  | Ο  | 0  | n  | Power Control         | 20  | 1 byte    |
| 23         | VOLCTRL  | 0  | 1  | 0  | 1  | 0  | 0  | 0  | 0  | 0  | 0  | 1  | EC control            | 81  | 2 bytes   |
| 24         | VOLUP    | 0  | 1  | 0  | 1  | 1  | 0  | 1  | 0  | 1  | 1  | 0  | EC increase 1         | D6  | None      |
| 25         | VOLDOWN  | υ  | 1  | υ  | 1  | 1  | υ  | 1  | U  | 1  | 1  | 1  | EC decrease 1         | D7  | None      |
| 26         | RESERVED | 0  | 1  | 0  | 1  | 0  | 0  | 0  | 0  | 0  | 1  | 0  | Not Use               | 82  | D         |
| 27         | EPSRRD1  | 0  | 1  | 0  | 0  | 1  | 1  | 1  | 1  | 1  | 0  | 0  | READ Register1        | 7C  | None      |



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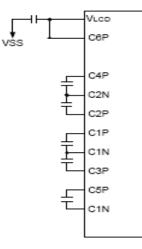
| Emmanana |         | - | anan a |   |   |   |   | 1.0.00 |     | 1.11.11.1 | 1.11.11 |   |                 | reasonaire<br>I | and search and search |
|----------|---------|---|--------|---|---|---|---|--------|-----|-----------|---------|---|-----------------|-----------------|-----------------------|
| 20       | EPSRRD2 | 0 | 1      | D | 0 | 1 | 1 | 1      | 1   | 1         | 0       | 1 | READ Register2  | 7D              | None                  |
| 29       | NOP     | 0 | 1      | D | 0 | 0 | 1 | 0      | 0   | 1         | 0       | 1 | NOP Instruction | 25              | None                  |
| 30       | STREAD  | 0 | 0      | 1 |   |   | F | Read   | Dat | ค         |         |   | Status Read     |                 |                       |
| 31       | EPINT   | 0 | 1      | D | 0 | 0 | 0 | 0      | 0   | 1         | 1       | 1 | Initial code(1) | 07              | 1 byte                |

Ext=1

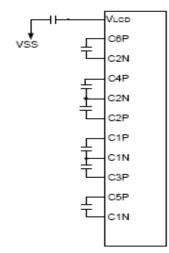
| Index | Command    | A٥ | RD | WR | D7 | D6 | D5 | D4 | DЗ | D2 | D1 | DO | Function              | Hex | Parameter |
|-------|------------|----|----|----|----|----|----|----|----|----|----|----|-----------------------|-----|-----------|
| 1     | Gray 1 Set | 0  | 1  | 0  | 0  | 0  | 1  | 0  | 0  | 0  | 0  | 0  | FRAME 1 Gray FWM Set  | 20  | 16 bytes  |
| 2     | Gray 2 Set | 0  | 1  | 0  | 0  | 0  | 1  | 0  | 0  | 0  | 0  | 1  | FRAME 2 Gray FWM Set  | 21  | 16 bytes  |
| з     | Wt. Set    | 0  | 1  | 0  | 0  | 0  | 1  | 0  | 0  | 0  | 1  | 0  | Weight Set            | 22  | 3 bytes   |
| 4     | ANASET     | 0  | 1  | 0  | 0  | 0  | 1  | 1  | 0  | ۵  | 1  | 0  | Analog Circuit Set    | 32  | 3 bytes   |
| 5     | DITHOFF    | Ο  | 1  | n  | Ω  | 0  | 1  | 1  | Û  | 1  | n  | 0  | Dithering Circuit Off | 34  | None      |
| 6     | DITHCN     | 0  | 1  | 0  | 0  | 0  | 1  | 1  | 0  | 1  | 0  | 1  | Dithering Circuit On  | 35  | None      |
| 7     | EPCTIN     | 0  | 1  | 0  | 1  | 1  | 0  | 0  | 1  | 1  | 0  | 1  | Control EEPROM        | CD  | 1 byte    |
| 8     | EPCOUT     | 0  | 1  | 0  | 1  | 1  | 0  | 0  | 1  | 1  | 0  | 0  | Cancel EEFROM         | сс  | None      |
| 9     | EPMWR      | 0  | 1  | 0  | 1  | 1  | 1  | 1  | 1  | 1  | 0  | 0  | Write to EEPROM       | гс  | None      |
| 10    | EPMRD      | 0  | 1  | 0  | 1  | 1  | 1  | 1  | 1  | 1  | 0  | 1  | Read from EEPROM      | FD  | None      |



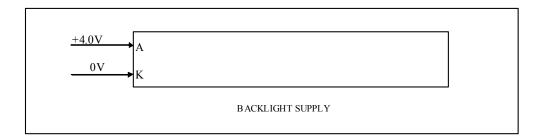
- 11. Power Supply
  - 11.1 6x Boosting



11.2 7x Boosting



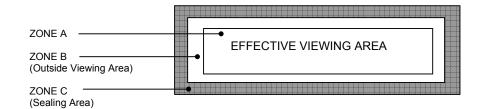
Cap = 1.0 uF to 2.2 uF





## 12.0 Quality Assurance

## **12.1 ZONE DEFINITION**



#### 12.2 <u>REJECTION CRITERIA</u>

#### 12.2.1 DIMENSIONAL DEFECTS

| Defect<br>Category             | Defect<br>Description   | Criterion | Drawing Specification                      |
|--------------------------------|---|-----------|--|
| Glass Size                     | Dimensions of<br>LCD, do not<br>conform to the<br>drawing       | Reject    | Refer to LCD Physical<br>Dimension Drawing |
| Perimeter<br>Seal<br>Extension | Perimeter seal<br>epoxy enters the<br>effective<br>viewing area | Reject    |  |
| End Seal<br>Size               | Size of end seal<br>does not meet<br>drawing<br>specification   | Reject    | Refer to LCD Physical<br>Dimension Drawing |

#### 12.2.2 VISUAL DEFECTS

| Defect<br>Category | Defect<br>Description  | Criterion  | Drawing Specification                       |
|--------------------|--|--|---|
| Fracture           | A type of<br>glass<br>breakage<br>containing<br>running<br>cracks.<br>Inspectors<br>should<br>attempt to<br>remove it<br>with<br>fingernail. If<br>removed,<br>evaluate as<br>chip | Reject – if the size is $\geq$ 30% of the contact ledge width. | S 30% of the<br>s 30% of the<br>ledge width |



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| Defect<br>Category | Defect<br>Description               | Criterion  | Drawing Specification   |
|--------------------|-------------------------------------|--|---|
| Chip               | Chip in cross<br>over area          | <ol> <li>Reject - if<br/>the chip<br/>causes<br/>crossover dot<br/>to be exposed</li> <li>Chip on<br/>outside edge</li> </ol>                      | Chip<br>Epoxy of<br>crossover dot<br>exposed  |
|                    |                                     | of the glass<br>plate but is<br>greater than<br>50% of glass<br>thickness at<br>crossover dot<br>is reject able.                                   |   |
| Chip               | Chip in<br>contact pad<br>area      | Accept if:-<br>a) $X \le 2.0$ mm<br>b) $Y \le 0.5$ mm<br>c) Z disregard  | Z<br>X  |
|                    | Chip in non-<br>contact pad<br>area | Accept if:-<br>a) $X \le 6.0$ mm<br>b) $Y \le 1.0$ mm<br>c) Z disregard  | z<br>x<br>y<br>x<br>y<br>x<br>x   |
|                    | Chip in<br>perimeter<br>seal area   | Accept if:-<br>a) $Y \le 1/3$ of<br>perimeter seal<br>width (W)<br>b) $X \le 3.0$ mm<br>c) Z disregard<br>d) X and Y not<br>touch<br>crossover dot | X<br>X<br>X<br>X<br>X<br>X<br>X<br>X<br>X<br>X<br>X<br>X<br>X<br>X<br>X<br>X<br>X<br>X<br>X |
| Corner Chip        | Corner chip<br>within seal<br>area  | Accept if:-<br>a) $X \le 1/3$ of<br>perimeter seal<br>width (W)<br>b) $Y \le 1/3$ of<br>perimeter seal<br>width (W)<br>c) Z disregard              |   |
|                    |                                     |  |   |



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| Defect      | Defect   | Criterion  | Drawing Specification                      |
|-------------|--|--|--|
| Category    | Description<br>Corner chip<br>not effecting<br>contact pad /<br>ITO                              | Accept if:-<br>a) $XY \le 4mm^2$<br>AND<br>b) $Y \le D$ and<br>$X \le 2.0mm$<br>c) Z disregard   |  |
|             | Corner chip<br>effecting<br>contact pad /<br>ITO   | A) Accept if:-<br>a) $XY \le 4mm^2$<br>AND<br>b) $Y \le D$ and<br>$X \le 2.0mm$<br>B) Accept if:-<br>a) $X1 \le 2.0mm$<br>b) $Y1 \le 0.5mm$<br>Z disregard | A B  |
| Glass flare | A thin layer<br>of glass flare<br>at contact<br>area   | Accept if:-<br>a) Flare<br>thickness ≤ ¼<br>W when W ≤<br>3mm<br>b) Flare<br>thickness ≤<br>1mm when W<br>> 3mm<br>W: Contact<br>ledge width               |  |
| Glass burr  | A rough<br>edge(s) left<br>along the<br>scribing edge<br>(i.e. along<br>the edges of<br>display) | Reject – if the<br>burr cause<br>undersize or<br>oversize of the<br>LCD  | Refer to LCD Physical Dimension<br>Drawing |
| Rainbow     | Colored ring<br>in sharp<br>blotches<br>observed   | Reject – if 3 or<br>more colored<br>rings in sharp<br>blotches of<br>color are<br>observed.<br>(Limit samples<br>should be used<br>when<br>applicable)     |  |



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| Defect                    | Defect                                | Criterion   | Drawing Specification |
|---------------------------|---------------------------------------|---|-----------------------|
| Category                  | Description                           |   |                       |
| Discoloration             |                                       | Reject - if the<br>discolorations<br>enter the<br>active viewing<br>area of LCD.<br>Color of the<br>LCD shall<br>follow product<br>specification<br>as specified in<br>the<br>manufacturing |                       |
| Air Void                  | LC does not<br>fulfill the<br>display | specification<br>Reject   |                       |
| Fill end<br>contamination | Discoloration<br>at end seal<br>area  | Reject if<br>discoloration<br>exceeded the<br>baffle (for<br>display with<br>baffle) or<br>viewing area<br>(for display<br>without baffle)  |                       |

#### 12.2.3 POLARIZER DEFECTS

| Defect<br>Category  | Defect Description                  | Criterion   | Drawing Specification                      |
|---------------------|-------------------------------------|---|--|
| Polarizer<br>defect | Polarizer coverage                  | <ol> <li>Polarizer should cover effective<br/>viewing area of display.</li> <li>It is acceptable if perimeter seal bolder<br/>at all sides could be seen.</li> <li>It is acceptable if polarizer attaching<br/>position meeting the tolerance<br/>mentioned in the drawing.</li> <li>It is reject able if polarizer edge jagged<br/>and not even</li> </ol> | Refer to LCD Physical<br>Dimension Drawing |
|                     | Polarizer Peeling /<br>delamination | 1-Reject if any edge or corner of the<br>polarizer is lifted up or not adheres to<br>the glass  |  |
|                     | Polarizer Scratches                 | <ol> <li>Any scratch should be acceptable if it is<br/>not visible from viewing distance at head<br/>of position</li> <li>Polarizer scratch in viewing area is<br/>reject able if it is visible from the<br/>specified viewing distance</li> <li>Defect, which is visible under surface<br/>glare, should be disregard</li> </ol>                           |  |



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| Defect<br>Category | Defect Description | Cı   | riterion                     | l       |                           | Drawing Specification |
|--------------------|--------------------|--|------------------------------|---------|---------------------------|-----------------------|
|                    | Polarizer damage   | 1-Stain mark or de<br>polarizer surface sl<br>is not visible from<br>head on position.<br>2-Defect, which is<br>glare, should be dis | hould t<br>viewin<br>visible |         |                           |                       |
|                    | Polarizer bubble / |  | 1                            |         |                           |                       |
|                    | Foreign material   | Zone /   | A                            | cceptał | ole No.                   | В                     |
|                    |                    | Dimension  | Α                            | В       | C                         |                       |
|                    |                    | $D \le 0.30 mm$  | NC                           | NC      | NC if                     | < A►                  |
|                    |                    | $D \le 0.50 mm$  | 2                            | NC      | the                       | D = (A + B)/2         |
|                    |                    | $0.50 < D \le 0.60$ mm   | 1                            | 2       | Polarize<br>r not         | D = (A + D)/2         |
|                    |                    | D > 0.60mm   | 0                            | 0       | lifted<br>up/ peel<br>off |                       |
|                    |                    | NC: No count<br>D: Mean Diameter   | of Det                       | fect    |                           |                       |
|                    |                    | 3 are the totally pe<br>bubble   | rmissit                      |         |                           |                       |

#### 12.2.4 ELECTRICAL TEST DEFECTS

| Defect<br>Category        | Defect Description  | Criterion  | Drawing Specification |
|---------------------------|---|--|-----------------------|
| Missing<br>common         | Part of the pattern does not light up                                   | Reject   |                       |
| Missing<br>segment        | One or few<br>segment does not<br>light up                              | Reject   |                       |
| Common-<br>common short   | Common and common connected   | Reject   |                       |
| Segment-<br>segment short | Segment and segment connected   | Reject   |                       |
| Common –<br>segment short | Common and segment connected  | Reject   |                       |
| Wrong<br>viewing angle    | Wrong viewing angle   | Reject if display viewing angle not<br>conform to customer requirement |                       |
| Metal residue             | Extra spot lights up at the border of the segment.                      | Accept if $\leq 0.20$ mm (mean diameter)                               |                       |
| Slow<br>response          | Response of the<br>display on one side<br>slower than the<br>other side | Reject if it is visible at 30cm distance                               |                       |



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| Defect<br>Category                 | Defect Description  | Crite  | erion  | Drawing Specification |
|------------------------------------|---|--|--|-----------------------|
| Pin Hole                           | Pin hole / void at<br>light up segment  | Zone /<br>Dimension<br>Located inside<br>single pixel/dot:-<br>$(X + Y)/2 \le 0.20$ mm<br>Laid over the plural<br>pixel/dots:<br>$(X + Y)/2 \le 0.20$ mm<br>(¾ or larger part of a<br>effective for display) | Acceptable No.<br>- 1 per pixel/dot<br>- 3 per display<br>(Active Area)<br>- 1 per pixel/dot<br>- 3 per display<br>(Active Area)<br>dot area has to be |                       |
| Deformed<br>display dot            | Lacked<br>deformation   | Accept if:<br>i) $X \le 0.15$ and<br>ii) $Y \le 0.15$  |  |                       |
|                                    | Added deformation   | Accept if:<br>i) X < 0.02 and<br>ii) Y < 0.02  |  |                       |
| Reverse twist/<br>tilt             | Segment are darker<br>or clearer than<br>other area of the<br>same segment        | Reject   |  |                       |
| Misalignment                       | Segment fatter or<br>smaller or extra<br>segment                                  | Reject if > 10% of do<br>width and visible at 3  | esigned segment<br>0cm distance  |                       |
| Segment<br>Smearing<br>Dim segment | Light up segment<br>smear<br>Display shows<br>poor contrast at pre<br>set voltage | Reject<br>Reject   |  |                       |



| Defect<br>Category                       | Defect Description                                | Crite   | erion | Drawing Specification |    |               |
|--|---|---|-------|-----------------------|----|---------------|
| Black Spot,<br>White Spot<br>and Foreign | Black Spot, White<br>Spot and Foreign<br>Material | Zone / Acceptable No.   |       |                       |    |               |
| 0  | waterial  | Dimension   | Α     | В                     | C  | Γ B           |
| Material                                 |   | $D \leq 0.10 \text{mm}$ NC NC NC  |       |                       |    |               |
|  |   | 0.10 <d 0.15mm<="" <="" td=""><td>3</td><td>3</td><td>NC</td><td>D = (A + B)/2</td></d> | 3     | 3                     | NC | D = (A + B)/2 |
|  |   | $0.15 < D \leq 0.25 mm$   | 1     | 2                     | NC |               |
|  |   | $0.25 < D \le 0.35 mm$  | 1     | 1                     | NC |               |
|  |   | D > 0.35 mm   | 0     | 0                     | NC |               |
|  |   |   |       |                       |    |               |
|  |   | D: Mean Diameter of Defect  |       |                       |    |               |

#### 12.2.5 BLACK SPOT, WHITE SPOT AND FOREIGN MATERIAL (SOLID FIGURE)

NOTE: The 1/3 or larger parts of individual dot has to be lighted on.

The solid figure is that the defect has clear-cut outline at the optimum driving condition in both positive and negative, of which size does not change when the contrast changes.

#### 12.2.6 BLACK SPOT, WHITE SPOT AND FOREIGN MATERIAL (FADED FIGURE)

| Defect<br>Category   | Defect Description                                | Crite   | erion         | Drawing Specification |
|--|---|---|---------------|-----------------------|
| Category<br>Black Spot,<br>White Spot<br>and Foreign<br>Material | Black Spot, White<br>Spot and Foreign<br>Material | Zone /<br>Dimension<br>$D \le 0.60$ mm<br>$0.60 < D \le 0.70$ mm<br>$0.70 < D \le 0.80$ mm<br>D > 0.80 mm<br>NC: No count | D = (A + B)/2 |                       |
|  |   | D: Mean Diameter of   | f Defect      |                       |

NOTE: Faded figure means that the defects has unclear outline at the optimum driving condition in both positive and negative, of which size seems to change when the contrast changes.



#### 12.2.7 LINE SHAPE AND SCRATCHES

| Defect<br>Category | Defect Description | Criterion       |             |                         |    | Drawing Specification |  |
|--------------------|--------------------|-----------------|-------------|-------------------------|----|-----------------------|--|
| Line shape         | Line shape and     |                 |             |                         |    |                       |  |
| and scratches      | scratches          | Zone /Dimension |             | Acceptable No.          |    |                       |  |
|                    |                    | Х               | Y           | Α                       | В  | С                     |  |
|                    |                    | NC              | ≤<br>0.03mm | NC                      | NC | NC                    |  |
|                    |                    | $\leq 2 \ mm$   | ≤<br>0.05mm | 1                       | 1  | NC                    |  |
|                    |                    | $\leq 1$ mm     | ≤<br>0.10mm | 1                       | 2  | NC                    |  |
|                    |                    | NC              | ≥<br>0.10mm | Due to (1) round defect |    |                       |  |
|                    |                    |                 |             |                         |    |                       |  |

NOTE: Length is X and Width is Y.

#### REMARK:

i) Total amount of spot defects including round and linear – A total of 5 permissible numbers of defects in Zone A & B including above (12.2.5), (12.2.6), (12.2.7). Regardless of number of defects, the minimum distance between individual defects have to be 5mm or larger.

ii) All the other items of inspection that are not included herein must be determined by the "Limit Standard" sample, which were occasionally set up with the mutual consent of both parties. In every case of the items set up with the Limit Standard, the Limit Standard always takes precedence over the other means of definition.



#### 13. Precaution for using LCM

#### 1. Liquid Crystal Display (LCD)

LCD is made up of glass, organic sealant, organic fluid and polymer based polarizers. The following precautions should be taken when handling.

- b) Keep the temperature within the range of use and storage. Excessive temperature and humidity could cause polarization degredation, polarizer peel off or bubble.
- c) Do not contact the exposed polarizer with anything harder than HB pencil lead. To clean dust off the display surface, wipe gently with cotton, chamois or other soft material soaked in petroleum benzin.
- d) Wipe off saliva or water drops immediately. Contact with water over a long period of time may cause polarizer deformation or colour fading, while an active LCD with water condensation on its surface will cause corrosion of ITO electrodes.
- e) Glass can be easily chipped or cracked from rough handling, especially at corners and edges.
- f) Do not drive LCD with DC voltage.

#### 2. Liquid Crystal Display Modules.

2.1 Mechanical Considerations

LCM are assembled and adjusted with a high degree of precision. Avoid excessive shocks and do not make any alterations or modification. The following should be noted.

- a) Do not tamper in any way with the tabs on the metal frame.
- b) Do not modify the PCB by drilling extra holes, changing its outline, moving its component or modifying its pattern.
- c) Do not touch the elastomer connector, especially insert a backlight panel (for example, EL)
- d) When mounting a LCM make sure that the PCB is not under any stress such as bending or twisting. Elastomer contacts are very delicate and missing pixels could result from slight dislocation of any of the elements.

 a) Avoid pressing on the metal bezel, otherwise the elastomer connector could be deformed and lose contact, resulting in missing pixels.

#### 2.2 Static Electricity

LCM contains CMOS LSI's and the same precaution for such devices should apply, namely

- a) The operator should be grounded whenever he/she comes into contact with the module. Never touch any of the conductive parts such as the LSI pads, the copper leads on the PCB and the interface terminals with any parts of the human body.
- b) The modules should be kept in antistatic bags or other containers to static for storage.
- c) Only properly grounded soldering irons should be used.
- d) If an electric screwdriver is used, it should be well grounded and shielded from commutator spark.
- e) The normal static prevention measures should be observed for work clothes and working benches, the latter conductive (rubber) mat is recommended.
- f) Since dry air is inductive to statics, a relative humidity of 50-60% is recommended.

#### 2.3 Soldering

- a) Solder only to the I/O terminals.
- b) Use only soldering irons with proper grounding and no leakage.
- c) Soldering temperature: 280 °C
- d) Soldering time: 3 to 4 sec
- e) Use eutectic solder with resin flux fill.
- f) If flux is used, the LCD surface should be covered to avoid flux spatters. Flux residue should be removed afterwards.



#### 2.4 Operation

- a) The contras can be adjusted by varying the LCD driving voltage V0
- b) Driving voltage should be kept within specified range, excess voltage shortens display life.
- c) Response time increases with decrease in temperature.
- d) Display may turn black or dark blue at temperature above its operational range, this is (however not pressing on the viewing area) may cause the segments to appear "fractured".
- e) Mechanical disturbance during operation ( such as pressing on the viewing area) may cause the segments to appear "fractured".

#### 2.5 Storage

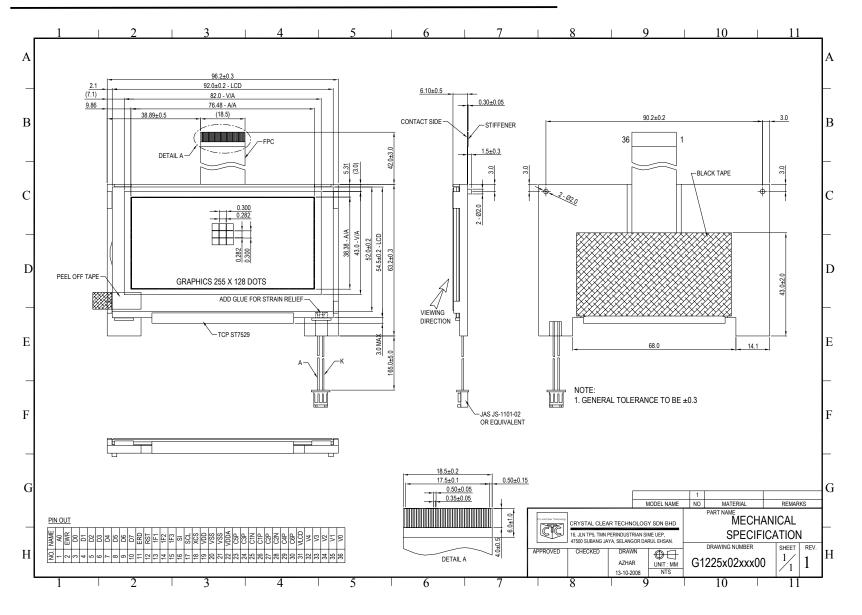
If any fluid leaks out of the damage glass cell, wash off any human part that comes into contact with soap and water. Never swallow the fluid. The toxicity is extremely low but caution should be exercised at all the time.

#### 2.6 Limited Warranty

Unless otherwise agreed between Crystal Clear Technology and customer, Crystal Clear Technology will replace or repair any of its LCD and LCM which is found to be defective electrically and visually when inspected in accordance with Crystal Clear Technology acceptance standards, for a period of one year from date of shipment. Confirmation of such date shall be based on freight documents. The warranty liability of Crystal Clear Technology is limited to repair and/or replacement on the terms set forth above. Crystal Clear Technology will not responsible for any subsequent or consequential events.

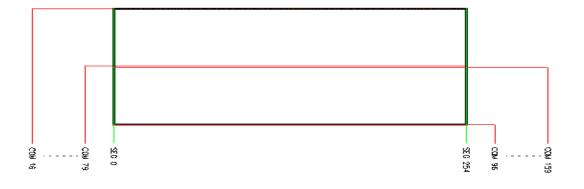


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LCD Segment and Common Layout



Crystal Clear Technology 16 Jalan TP5—Taman Perindustrian Sime UEP 47600 Subang Jaya—Selangor DE Malaysia